Loop Quasi-Invariant Chunk Motion

by peeling with statement composition

Thomas Rubiano

From an idea of L. Kristiansen
supervised by J. Y. Moyen
in collaboration with T. Seiller
funded by the Elica project

Implicit Computational Complexity implementation in Compilers

ICC techniques in a loop optimization

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Welcome to the real world !

- This work shows that we can do something in real ~~world~~ languages
- Using data flow analysis seen in ICC papers (“size-change graphs” and “*mwp*-bounds”)

Motivations

Loop-Invariant

```
int x=rand()%100;
while(i<100){
    y=x+x; //invariant
    use(y);
    i=i+1;
}
```

Loop-Invariant

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int x=rand()%100;
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    y=x+x; //invariant
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Loop-Invariant

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    i=i+1;
}
while(i<100){
    use(y);
    i=i+1;
}
```

- Obviously already in compilers : called "*Loop Invariant Code Motion*" (**8126** instructions hoisted over **3870** loops in *vim*...)

Loop-Quasi-Invariants

```
while(i<100){
    z=y*y; //quasi-invariant
    use(z);
    y=x+x; //invariant
    use(y);
    i=i+1;
}
```

Loop-Quasi-Invariants

```
while(i<100){
    z=y*y; //quasi-invariant
    use(z);
    y=x+x; //invariant
    use(y);
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}
```

```
if(i<100){
  z=y*y;
  use(z);
  y=x+x;
  use(y);
  i=i+1;
}
if(i<100){
  z=y*y;
  use(z);
  use(y);
  i=i+1;
}
while(i<100){
  use(z);
  use(y);
  i=i+1;
}
```

Loop-Quasi-Invariants

```
while(i<100){
    z=y*y; //quasi-invariant
    use(z);
    y=x+x; //invariant
    use(y);
    i=i+1;
}
```

```
if(i<100){
  z=y*y;
  use(z);
  y=x+x;
  use(y);
  i=i+1;
}
if(i<100){
  z=y*y;
  use(z);
  use(y);
  i=i+1;
}
while(i<100){
  use(z);
  use(y);
  i=i+1;
}
```

- Peeling is removing instructions out of the loop while unrolling it
- Done but not done. . .

  . . . regarding to invariants (only loop size and trip count) !

Loop-Quasi-Invariant Chunks

```
while(j<100){
    fact=1;
    i=1;
    while(i<=n){
        fact=fact*i;
        i=i+1;
    }
    use(fact);
    j=j+1;
}
```

Loop-Quasi-Invariant Chunks

```
while(j<100){
    fact=1;
    i=1;
    while(i<=n){
        fact=fact*i;
        i=i+1;
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```

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  use(fact);
  j=j+1;
}
```

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```
if(j<100){
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  i=1;
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  }
  use(fact);
  j=j+1;
}
while(j<100){
  use(fact);
  j=j+1;
}
```

Loop-Quasi-Invariant Chunks

```
while(j<100){
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}
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if(j<100){
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  }
  use(fact);
  j=j+1;
}
while(j<100){
  use(fact);
  j=j+1;
}
```

- Definitely new! (at least in GCC and LLVM)

Introduction

A `WHILE`-language

(Variables)	X	$::=$	$X_1 \mid X_2 \mid X_3 \mid \ldots \mid X_n$
(Expression)	*exp*	$::=$	$X \mid \texttt{op(}\mathit{exp}\texttt{,...,}\mathit{exp}\texttt{)}$
(Command)	*com*	$::=$	$X\texttt{=}\mathit{exp} \mid \mathit{com}\texttt{;}\mathit{com} \mid \texttt{skip} \mid$
			$\texttt{while}\ \mathit{exp}\ \texttt{do}\ \mathit{com} \mid$
			$\texttt{if}\ \mathit{exp}\ \texttt{then}\ \mathit{com}\ \texttt{else}\ \mathit{com} \mid$

A `WHILE`-language

(Variables)	X	: :=	$X_1 \mid X_2 \mid X_3 \mid \ldots \mid X_n$
(Expression)	*exp*	: :=	$X \mid \texttt{op(}\mathit{exp}\texttt{,...,}\mathit{exp}\texttt{)}$
(Command)	*com*	: :=	$X\texttt{=}\mathit{exp} \mid \mathit{com}\texttt{;}\mathit{com} \mid \texttt{skip} \mid$
			$\texttt{while}\ \mathit{exp}\ \texttt{do}\ \mathit{com} \mid$
			$\texttt{if}\ \mathit{exp}\ \texttt{then}\ \mathit{com}\ \texttt{else}\ \mathit{com} \mid$
			$\texttt{use(}X_1\texttt{,...,}X_n\texttt{)}$

Quasi-Invariants

- A quasi-invariant is a variable with a value which does not change after a certain number of loop execution
- A degree of invariance is the number of time we need to iterate the loop until the variable is stable

```
while(i<100){
    z=y*y; // 2
    use(z);
    y=x+x; // 1
    use(y);
    i=i+1;
}
```

Theory : Data Flow Graph

Definition

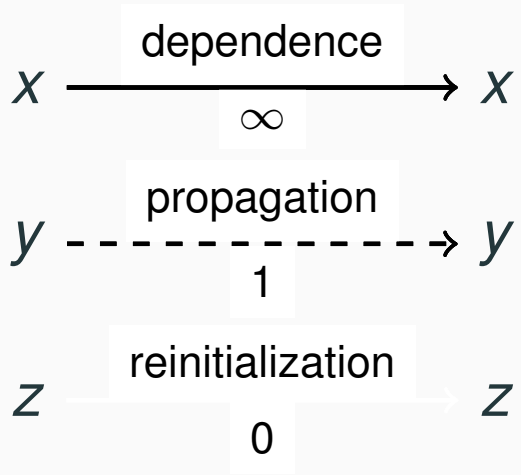
Definition (Data Flow Graph)

A Data Flow Graph represents dependencies between variables as a bipartite graph as below.

$$\mathrm{c} := [x = x + 1;$$

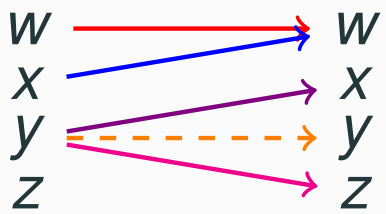
$$y = y;$$

$$z = 0;]$$



Matrix algebra

A *Data Flow Graph* for a command $\mathtt{c}$ is a $n \times n$ matrix over the semi-ring $\{0, 1, \infty\}$.



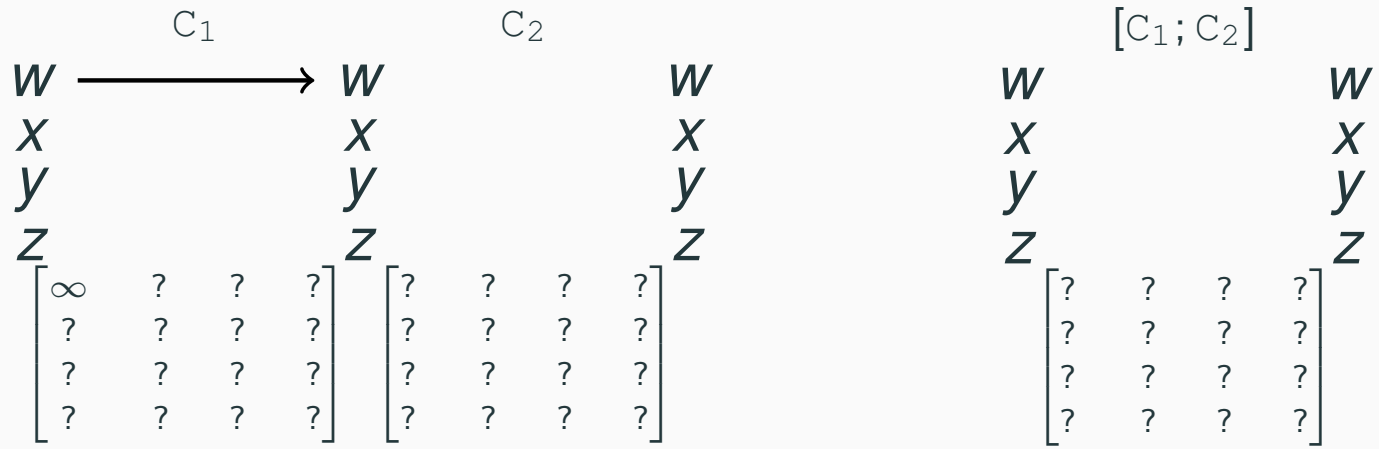
$\rightarrow$

$$\begin{bmatrix} \infty & 0 & 0 & 0 \\ \infty & 0 & 0 & 0 \\ 0 & \infty & 1 & \infty \\ 0 & 0 & 0 & 0 \end{bmatrix}$$

We write $+$ and $\times$ the two operations (*max*, *times*).

Multipath and Composition (*à la "size-change Termination"*)

Let $\mathtt{C}$ be a sequence of commands $[\mathtt{C}_1; \mathtt{C}_2; \ldots; \mathtt{C}_n]$. Then $\mathbf{M}(\mathtt{C})$ is defined as the matrix product $\mathbf{M}(\mathtt{C}_1)\mathbf{M}(\mathtt{C}_2)\ldots\mathbf{M}(\mathtt{C}_n)$.



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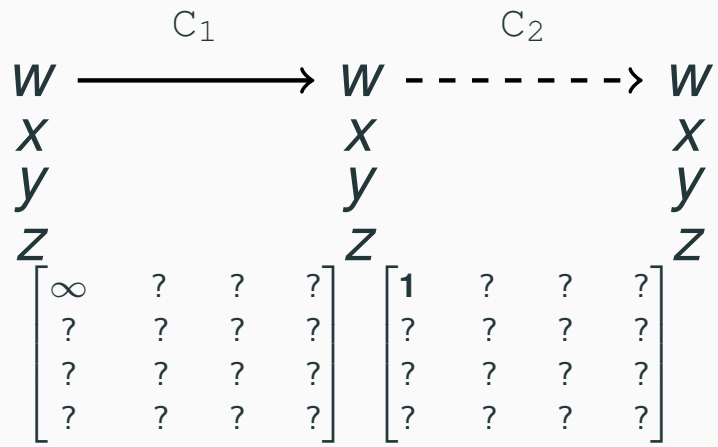

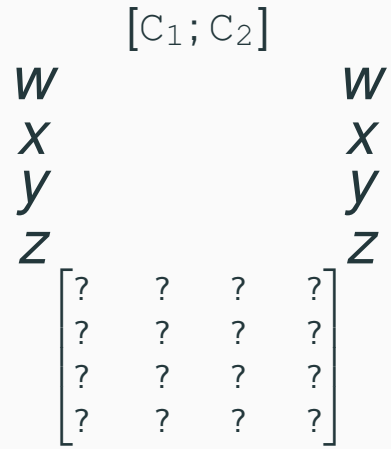

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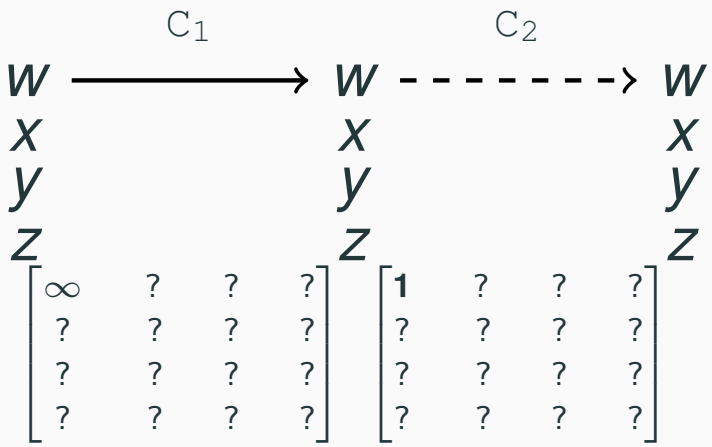

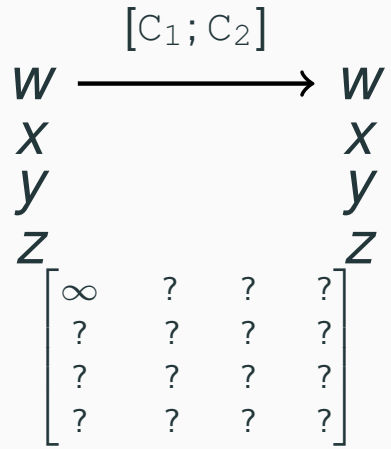

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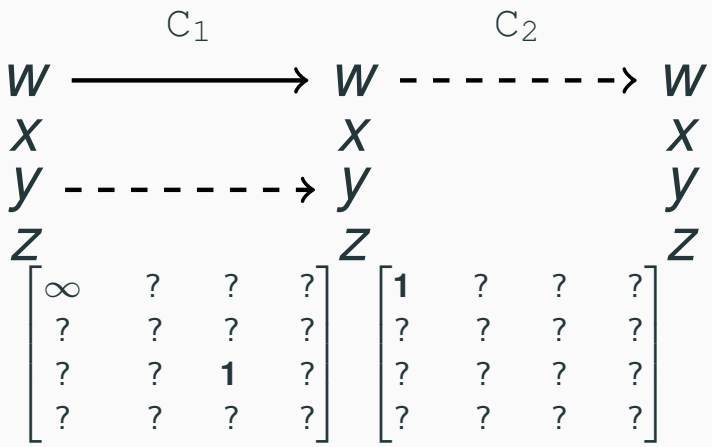

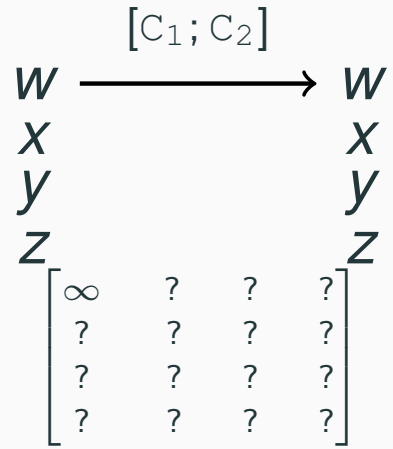

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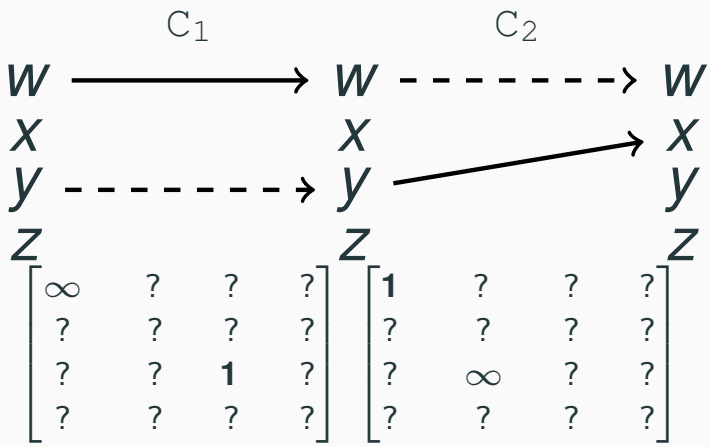

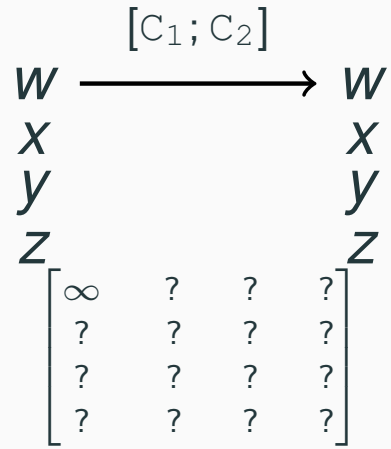

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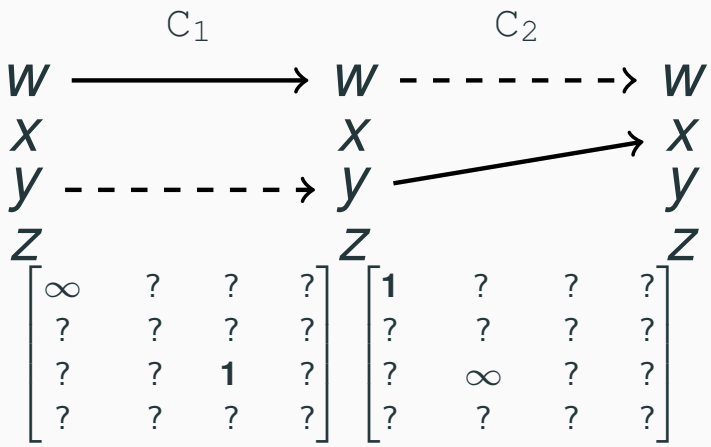

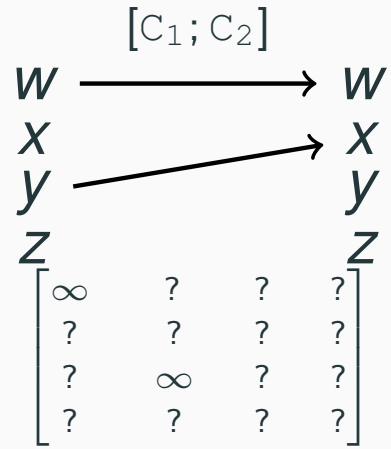

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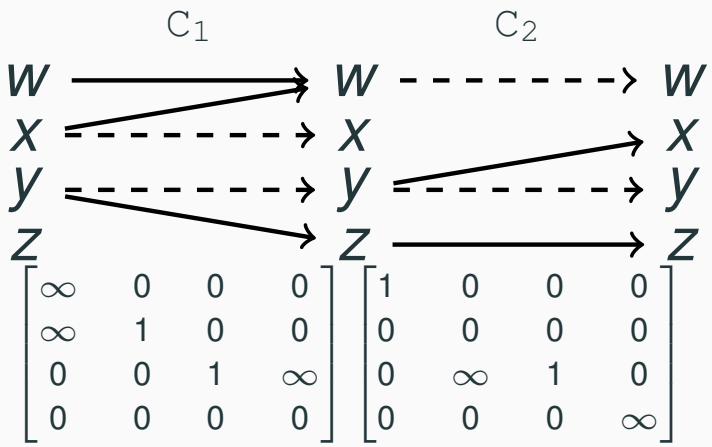
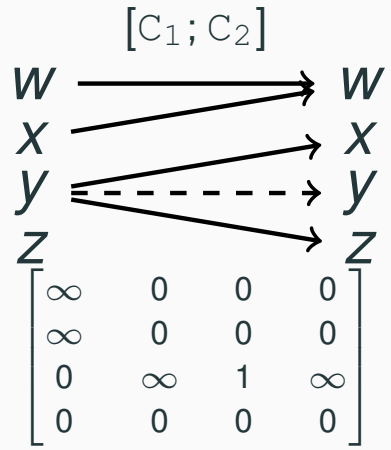
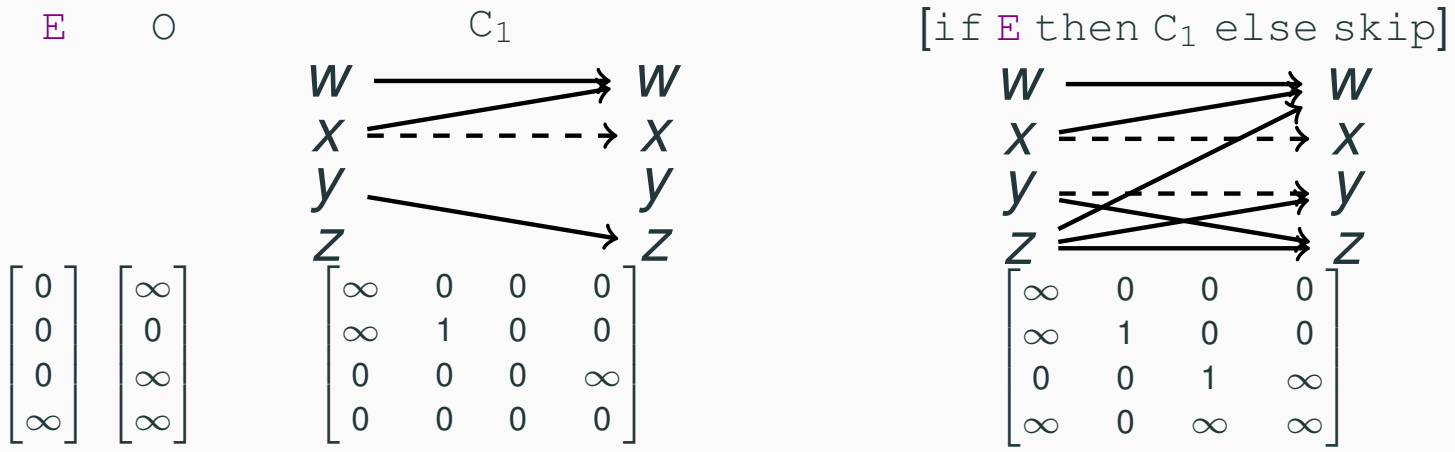




Condition

Let $\mathtt{C}$ be a command of the form $\mathtt{if\ E\ then\ C_1\ else\ skip}$;
Then $\mathrm{M}(\mathtt{C}) = \mathrm{M}(\mathtt{C_1}) + \mathrm{Id} + (E^t O)$



Compute the max DFG regarding to the different possibilities.
$\mathrm{Var}(\mathtt{E})$ indirectly influence the dependencies.

Loop `while` (à la *"mwp-polynomials"*)

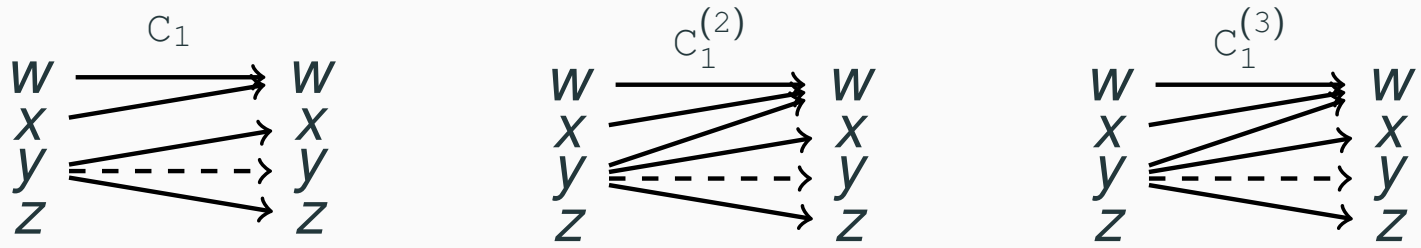
Let $\mathtt{C}$ be a command such as : $\mathtt{C} := \mathtt{while\ E\ do\ C_1;}$

- Number of iteration unknown :

$$\mathtt{skip} \qquad \mathtt{C_1} \qquad \mathtt{C_1;C_1} \qquad \mathtt{C_1;C_1;C_1} \qquad \text{etc...}$$

- Compute the max :

$$\mathrm{M}(\mathtt{C}_1^*)$$



Trivially converges by motonicity

Loop `while` (à la *"mwp-polynomials"*)

Let $\mathtt{C}$ be a command such as : $\mathtt{C} := \mathtt{while}\ \mathtt{E}\ \mathtt{do}\ \mathtt{C}_1;$

As for the `if` statement, a condition correction is needed.

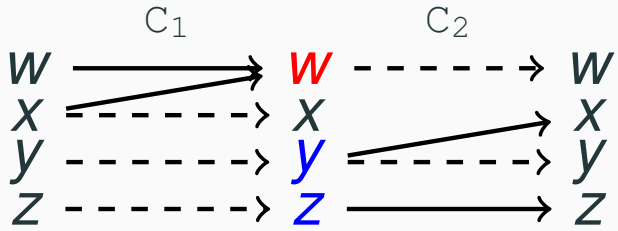
$$\mathbf{M}(\mathtt{C}) = \mathbf{M}(\mathtt{C}_1^*)^{[\mathtt{E}]}.$$

Theory : Independence

Independence of chunks

Definition (independence)

If $\mathrm{Out}(\mathtt{C}_1) \cap \mathrm{In}(\mathtt{C}_2) = \emptyset$ *then* $\mathtt{C}_2$ *is* ***independent*** *from* $\mathtt{C}_1$*. This is denoted* $\mathtt{C}_1 \prec \mathtt{C}_2$*.*



Definition (self-independence)

If $\mathtt{C}_1$ *is independent from itself, we say* $\mathtt{C}_1$ *is* ***self-independent***

Lemma (Optimization for `while`)

If $\mathtt{C}_1$ *is self-independent and* $\mathrm{Var}(\mathtt{E}) \cap \mathrm{Out}(\mathtt{C}_1) = \emptyset$ *:*

$[\![\mathtt{while\ E\ do\ [C_1]}]\!] \equiv [\![\mathtt{if\ E\ then\ [C_1];While\ E\ do\ [skip]}]\!]$

Moving Independent Chunks

Definition (Mutual Independence)

If $\mathtt{C}_2 \prec \mathtt{C}_1$ *and* $\mathtt{C}_1 \prec \mathtt{C}_2$*, we say that* $\mathtt{C}_2$ *and* $\mathtt{C}_1$ *are* ***mutually independents****, and write* $\mathtt{C}_1 \asymp \mathtt{C}_2$*.*

Lemma (Swapping commands)

If $\mathtt{C}_1 \asymp \mathtt{C}_2$*, then :*

$$[\![\mathtt{C}_1 ; \mathtt{C}_2]\!] \equiv [\![\mathtt{C}_2 ; \mathtt{C}_1]\!]$$

Lemma (Hoisting mutual independent commands)

If $\mathtt{C}_1$ *is self-independent (i.e.* $\mathtt{C}_1 \asymp \mathtt{C}_1$*),* $\mathrm{Var}(\mathtt{E}) \cap \mathrm{Out}(\mathtt{C}_1) = \emptyset$*, and if* $\mathtt{C}_1 \asymp \mathtt{C}_2$*, then :*

$$[\![\mathtt{while\ E\ do\ [C_1 ; C_2]}]\!] \equiv [\![\mathtt{if\ E\ then\ [C_1] ; while\ E\ do\ [C_2]}]\!]$$

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If $\mathtt{C}_2 \prec \mathtt{C}_1$ *and* $\mathtt{C}_1 \prec \mathtt{C}_2$*, we say that* $\mathtt{C}_2$ *and* $\mathtt{C}_1$ *are* ***mutually independents****, and write* $\mathtt{C}_1 \asymp \mathtt{C}_2$*.*

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$$[\![\mathtt{while\ E\ do\ [C_1 ; C_2]}]\!] \equiv [\![\mathtt{if\ E\ then\ [C_1 ; C_2] ; while\ E\ do\ [C_2]}]\!]$$

Computation of the invariance degree

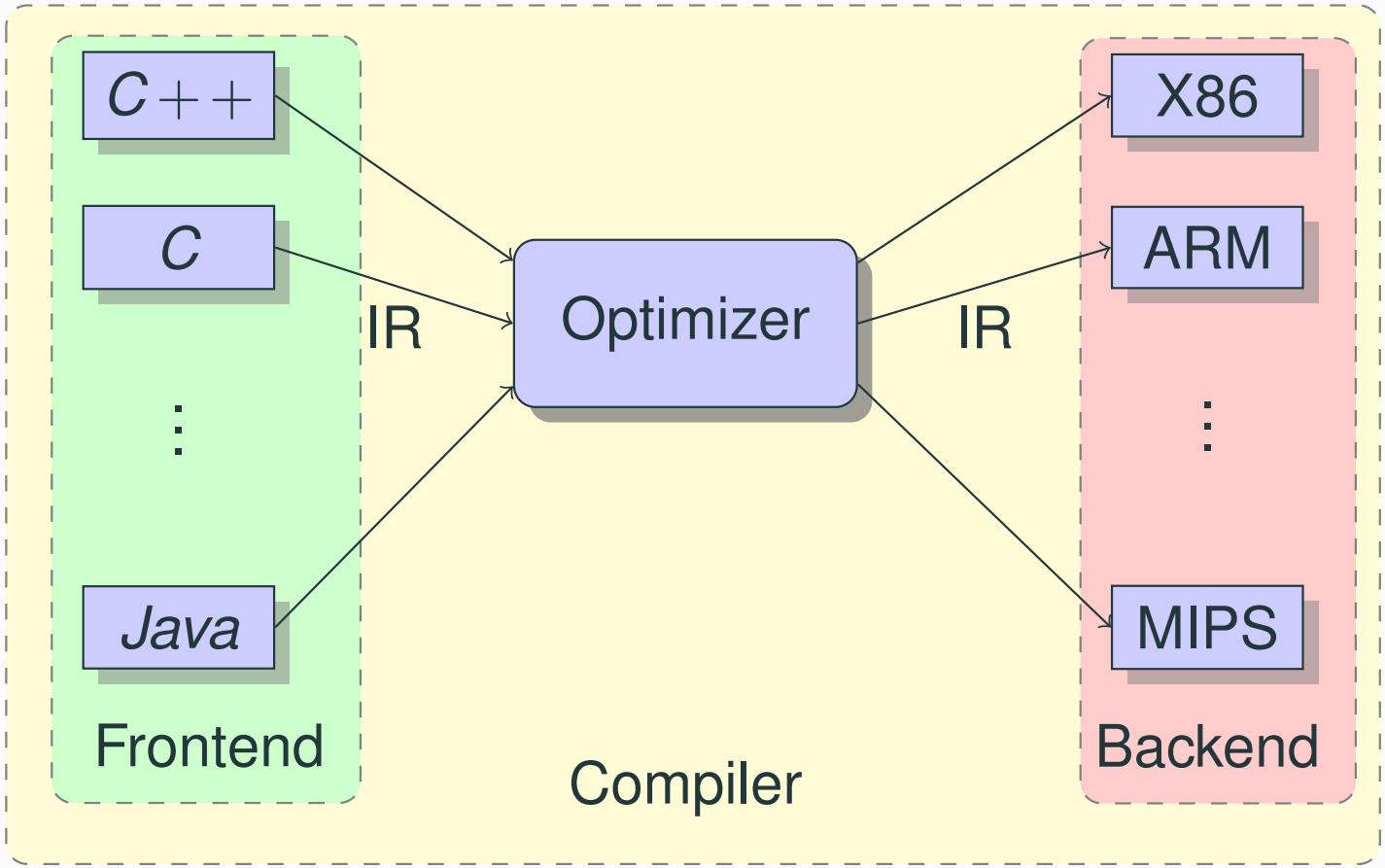
Statically easy using the DFGs and the dominance graph (order of the instructions).

Let suppose we have computed the graph of dependencies for all commands.

Compute recursively (depth-first order) the degrees of the depended commands and take the maximum.

Compiler

Architecture



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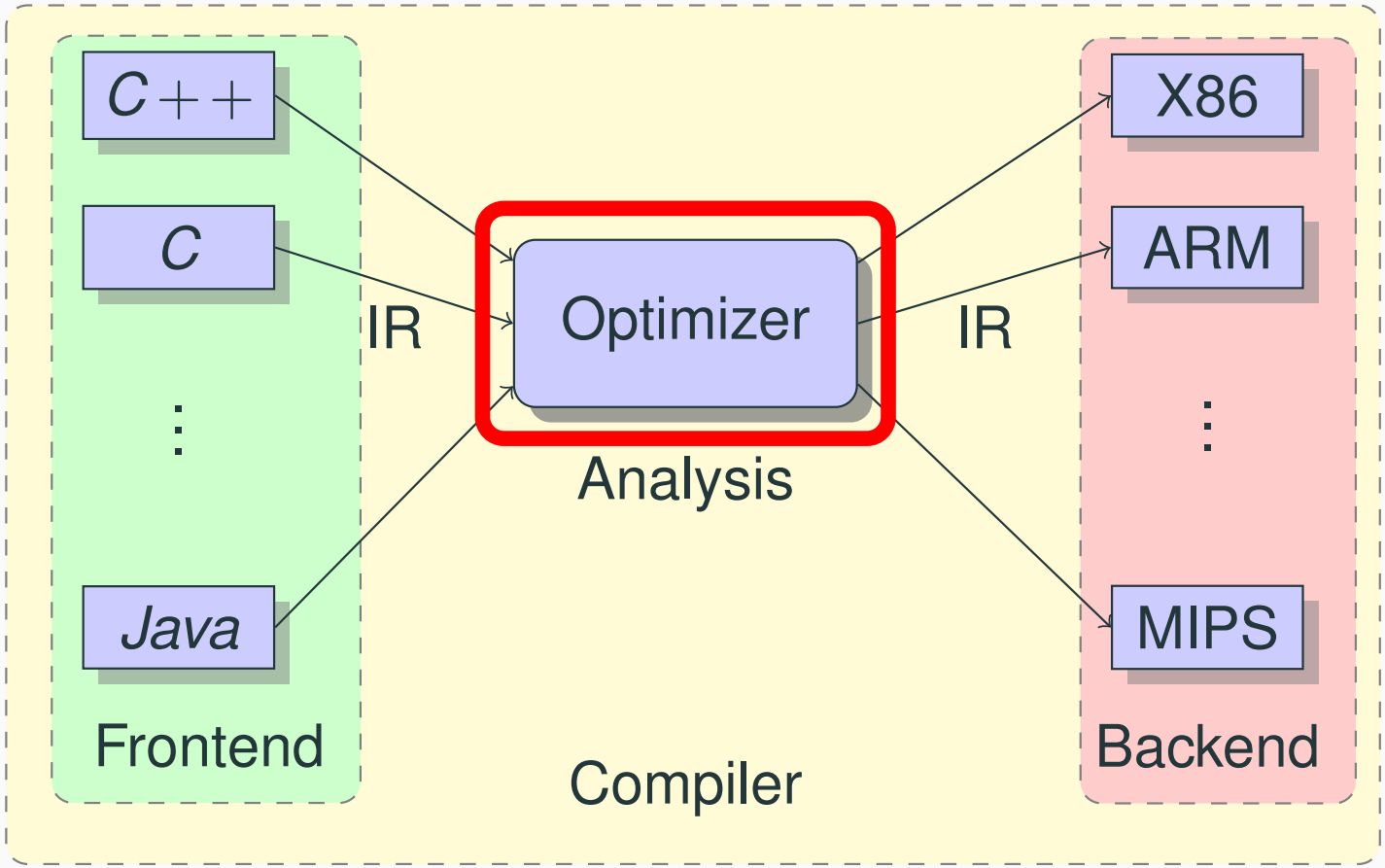

Order

Order is given as argument to the **pass manager** :

```
$ clang -Oz test.c
```

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```
Pass Arguments: -tti -targetlibinfo -tbaa -scoped-noalias -assumption-cache-tracker
     -profile-summary-info -forceattrs -inferattrs -ipsccp -globalopt -domtree
     -mem2reg -deadargelim -domtree -basicaa -aa -instcombine -simplifycfg
     -pgo-icall-prom -basiccg -globals-aa -prune-eh -inline -functionattrs -domtree
     -sroa -early-cse -lazy-value-info -jump-threading -correlated-propagation
     -simplifycfg -domtree -basicaa -aa -instcombine -tailcallelim -simplifycfg
     -reassociate -domtree -loops -loop-simplify -lcssa-verification -lcssa
     -basicaa -aa -scalar-evolution -licm -loop-unswitch -simplifycfg -domtree
     -basicaa -aa -instcombine -loops -loop-simplify -lcssa-verification -lcssa
     -scalar-evolution -indvars -loop-idiom -loop-deletion -loop-unroll
     -mldst-motion -aa -memdep -lazy-branch-prob -lazy-block-freq
     -opt-remark-emitter -gvn -basicaa -aa -memdep -memcpyopt -sccp -domtree
     -demanded-bits -bdce -basicaa -aa -instcombine -lazy-value-info
     -jump-threading -correlated-propagation -domtree -basicaa -aa -memdep -dse
     -loops -loop-simplify -lcssa-verification -lcssa -aa -scalar-evolution -licm
     -postdomtree -adce -simplifycfg -domtree -basicaa -aa -instcombine -barrier
     -elim-avail-extern -basiccg -rpo-functionattrs -globals-aa -float2int -domtree
     -loops -loop-simplify -lcssa-verification -lcssa -basicaa -aa
     -scalar-evolution -loop-accesses -lazy-branch-prob -lazy-block-freq
     -opt-remark-emitter -loop-distribute -loop-simplify -lcssa-verification -lcssa
     -branch-prob -block-freq -scalar-evolution -basicaa -aa -loop-accesses
     -demanded-bits -lazy-branch-prob -lazy-block-freq -opt-remark-emitter
     -loop-vectorize -loop-simplify -scalar-evolution -aa -loop-accesses
     -loop-load-elim -basicaa -aa -instcombine -scalar-evolution -demanded-bits
     -slp-vectorizer -simplifycfg -domtree -basicaa -aa -instcombine -loops
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      -profile-summary-info -forceattrs -inferattrs -ipsccp -globalopt -domtree
      -mem2reg -deadargelim -domtree -basicaa -aa -instcombine -simplifycfg
      -pgo-icall-prom -basiccg -globals-aa -prune-eh -inline -functionattrs -domtree
      -sroa -early-cse -lazy-value-info -jump-threading -correlated-propagation
      -simplifycfg -domtree -basicaa -aa -instcombine -tailcallelim -simplifycfg
      -reassociate -domtree -loops -loop-simplify -lcssa-verification -lcssa
      -basicaa -aa -scalar-evolution -licm -loop-unswitch -simplifycfg -domtree
      -basicaa -aa -instcombine -loops -loop-simplify -lcssa-verification -lcssa
      -scalar-evolution -indvars -loop-idiom -loop-deletion -loop-unroll
      -mldst-motion -aa -memdep -lazy-branch-prob -lazy-block-freq
      -opt-remark-emitter -gvn -basicaa -aa -memdep -memcpyopt -sccp -domtree
      -demanded-bits -bdce -basicaa -aa -instcombine -lazy-value-info
      -jump-threading -correlated-propagation -domtree -basicaa -aa -memdep -dse
      -loops -loop-simplify -lcssa-verification -lcssa -aa -scalar-evolution -licm
      -postdomtree -adce -simplifycfg -domtree -basicaa -aa -instcombine -barrier
      -elim-avail-extern -basiccg -rpo-functionattrs -globals-aa -float2int -domtree
      -loops -loop-simplify -lcssa-verification -lcssa -basicaa -aa
      -scalar-evolution -loop-accesses -lazy-branch-prob -lazy-block-freq
      -opt-remark-emitter -loop-distribute -loop-simplify -lcssa-verification -lcssa
      -branch-prob -block-freq -scalar-evolution -basicaa -aa -loop-accesses
      -demanded-bits -lazy-branch-prob -lazy-block-freq -opt-remark-emitter
      -loop-vectorize -loop-simplify -scalar-evolution -aa -loop-accesses
      -loop-load-elim -basicaa -aa -instcombine -scalar-evolution -demanded-bits
      -slp-vectorizer -simplifycfg -domtree -basicaa -aa -instcombine -loops
```

LLVM Intermediate Representation

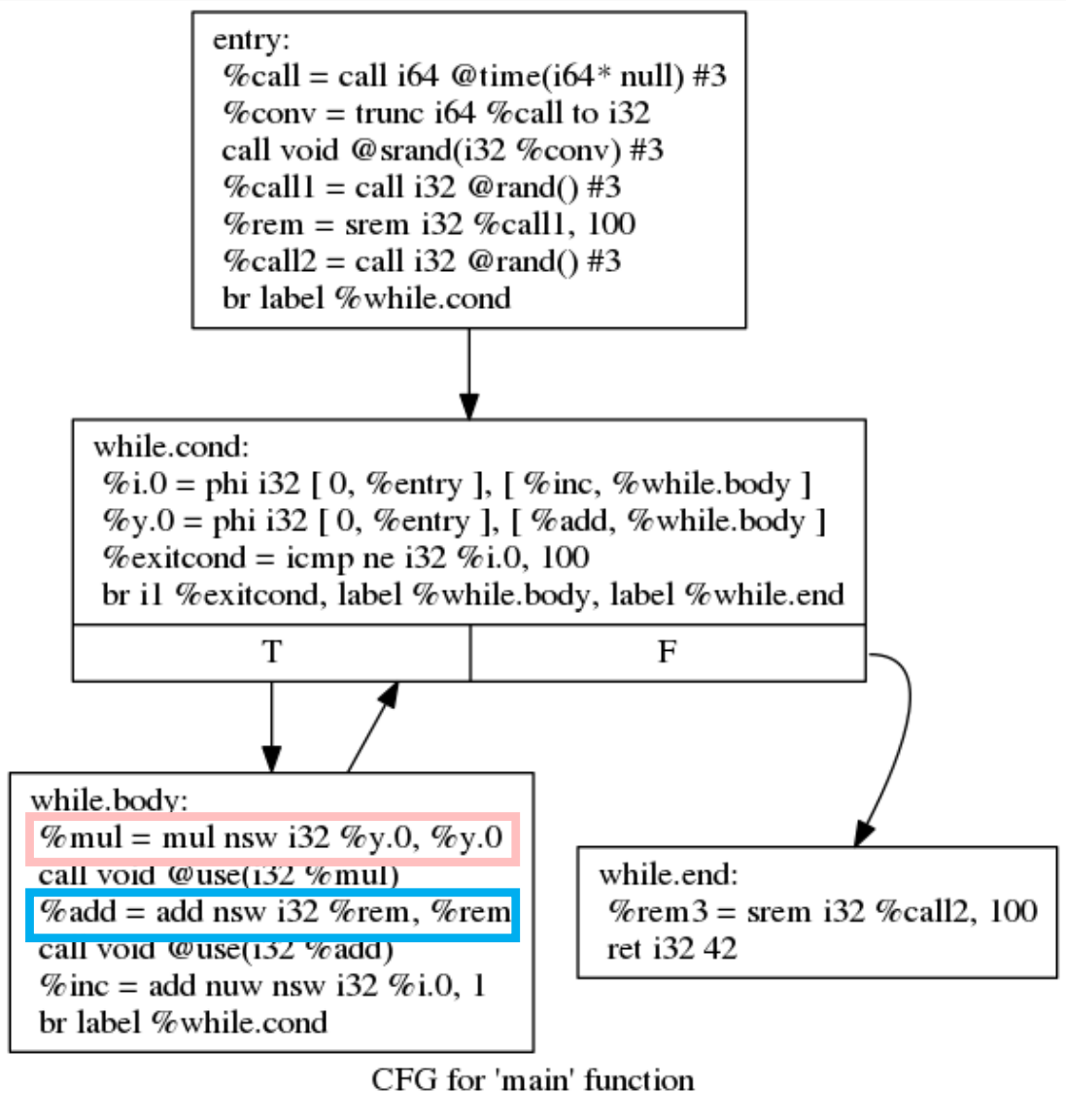
- LLVM-IR is a **Typed Assembly Language** (TAL) and a **Static Single Assignment** (SSA) based representation. This provides :
- An IR is **source-language-independent**, then optimizations and analysis should work on every languages (properly translated to this IR).

LLVM Intermediate Representation

```
define i32 @main() #0 {
entry:
  %call = call i64 @time(i64* null) #3
  %conv = trunc i64 %call to i32
  call void @srand(i32 %conv) #3
  %call1 = call i32 @rand() #3
  %rem = srem i32 %call1, 100
  %call2 = call i32 @rand() #3
  br label %while.cond

while.cond:
  %i.0 = phi i32 [ 0, %entry ], [ %inc, %while.body ]
  %y.0 = phi i32 [ 0, %entry ], [ %add, %while.body ]
  %exitcond = icmp ne i32 %i.0, 100
  br i1 %exitcond, label %while.body, label %while.end

while.body:
  %mul = mul nsw i32 %y.0, %y.0
  call void @use(i32 %mul)
  %add = add nsw i32 %rem, %rem
  call void @use(i32 %add)
  %inc = add nuw nsw i32 %i.0, 1
  br label %while.cond

while.end:
  %rem3 = srem i32 %call2, 100
  ret i32 42
}
```

```
int main(){
    int i=0,y=0;
    srand(time(NULL));
    int x=rand()%100;
    int x2=rand()%100;
    int z;
    while(i<100){
        z=y*y ;
        use(z);
        y=x+x ;
        use(y);
        i++;
    }
    return 42;
}
```

LLVM Intermediate Representation in Control Flow Graph



CFG for 'main' function

```
int main(){
    int i=0,y=0;
    srand(time(NULL));
    int x=rand()%100;
    int x2=rand()%100;
    int z;
    while(i<100){
        z=y*y ;
        use(z);
        y=x+x ;
        use(y);
        i++;
    }
    return 42;
}
```

Implementation

A prototype on LLVM tool chain

We implemented a pass in LLVM :

- Currently around 3000 lines of C++, and counting. . .
- tested on several relevant examples
- generates statistics while compiling. . .

Analysis : Degree on each Instruction

```
...
while.body:
  %mul = mul nsw i32 %y.0, %y.0          ← 2
  call void @use(i32 %mul)               ← ∞
  %add = add nsw i32 %rem, %rem          ← 1
  call void @use(i32 %add)               ← ∞
  %inc = add nuw nsw i32 %i.0, 1         ← ∞
  br label %while.cond                   ← ∞
...
```

```
...
    while(i<100){
        z=y*y ;
        use(z);
        y=x+x ;
        use(y);
        i++;
    }
...
```

Analysis : Degree on each Instruction

```
...
while.body:
  %mul = mul nsw i32 %y.0, %y.0          ← 2
  call void @use(i32 %mul)               ← ∞
  %add = add nsw i32 %rem, %rem          ← 1
  call void @use(i32 %add)               ← ∞
  %inc = add nuw nsw i32 %i.0, 1         ← ∞
  br label %while.cond                   ← ∞
...
```

```
...
    while(i<100){
        z=y*y ;
        use(z);
        y=x+x ;
        use(y);
        i++;
    }
...
```

Consider all `call` as anchors

Same example

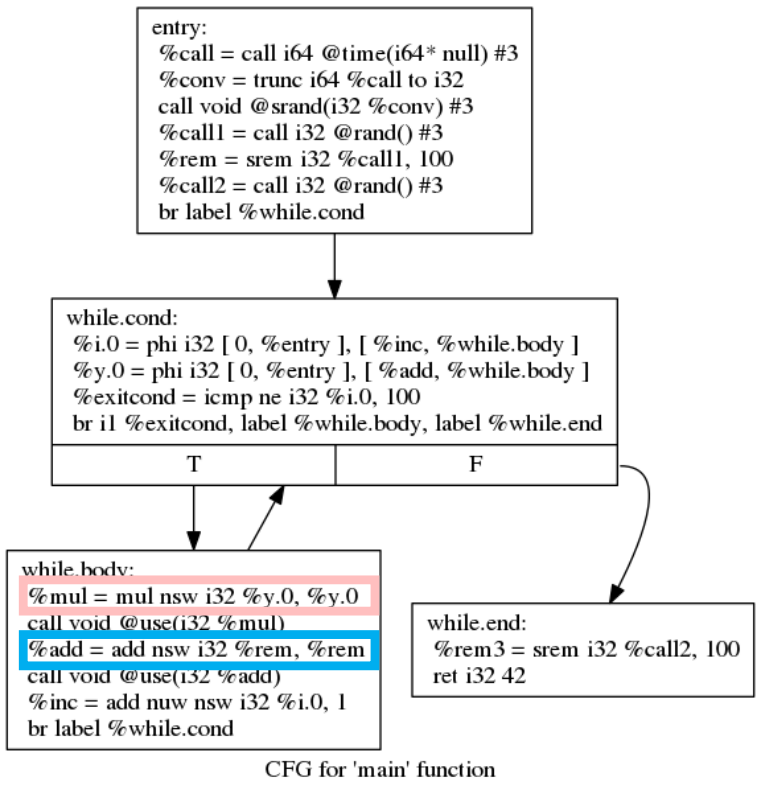
```
int main(){
    int i=0,y=0;
    srand(time(NULL));
    int x=rand()%100;
    int x2=rand()%100;
    int z;
    while(i<100){
        z=y*y ;
        use(z);
        y=x+x ;
        use(y);
        i++;
    }
    return 42;
}
```

Same example

```
int main(){
    int i=0,y=0;
    srand(time(NULL));
    int x=rand()%100;
    int x2=rand()%100;
    int z;
    while(i<100){
        z=y*y ;
        use(z);
        y=x+x ;
        use(y);
        i++;
    }
    return 42;
}
```

```
int main(){
    int i=0,y=0;
    srand(time(NULL));
    int x=rand()%100;
    int x2=rand()%100;
    int z;
    if(i<100){
        z=y*y ;
        use(z);
        y=x+x ;
        use(y);
        i=i+1;
    }
    if(i<100){
        z=y*y ;
        use(z);
        use(y);
        i=i+1;
    }
    while(i<100){
        use(z);
        use(y);
        i=i+1;
    }
    return 42;
}
```

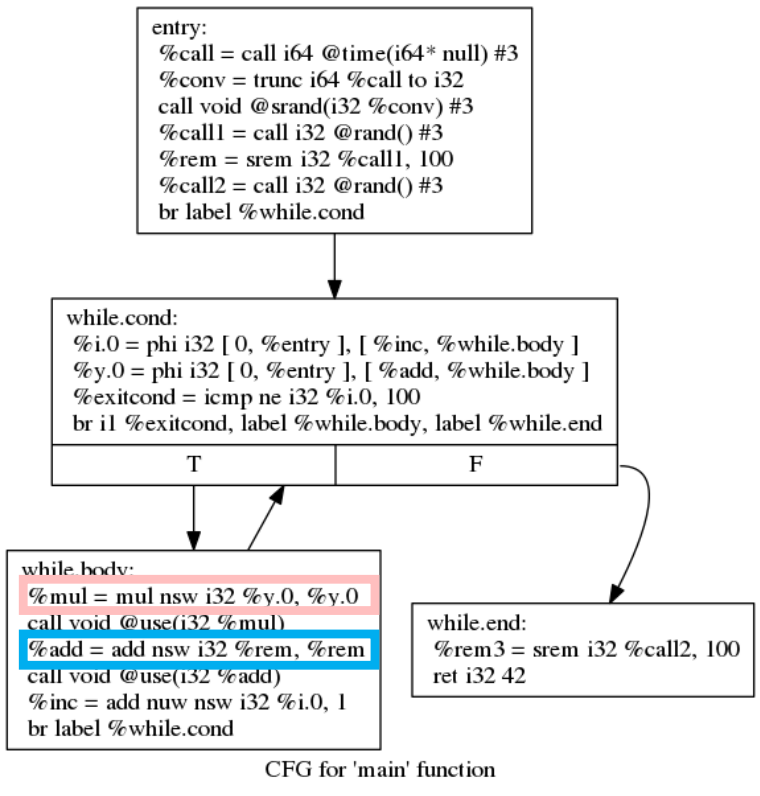
Same example



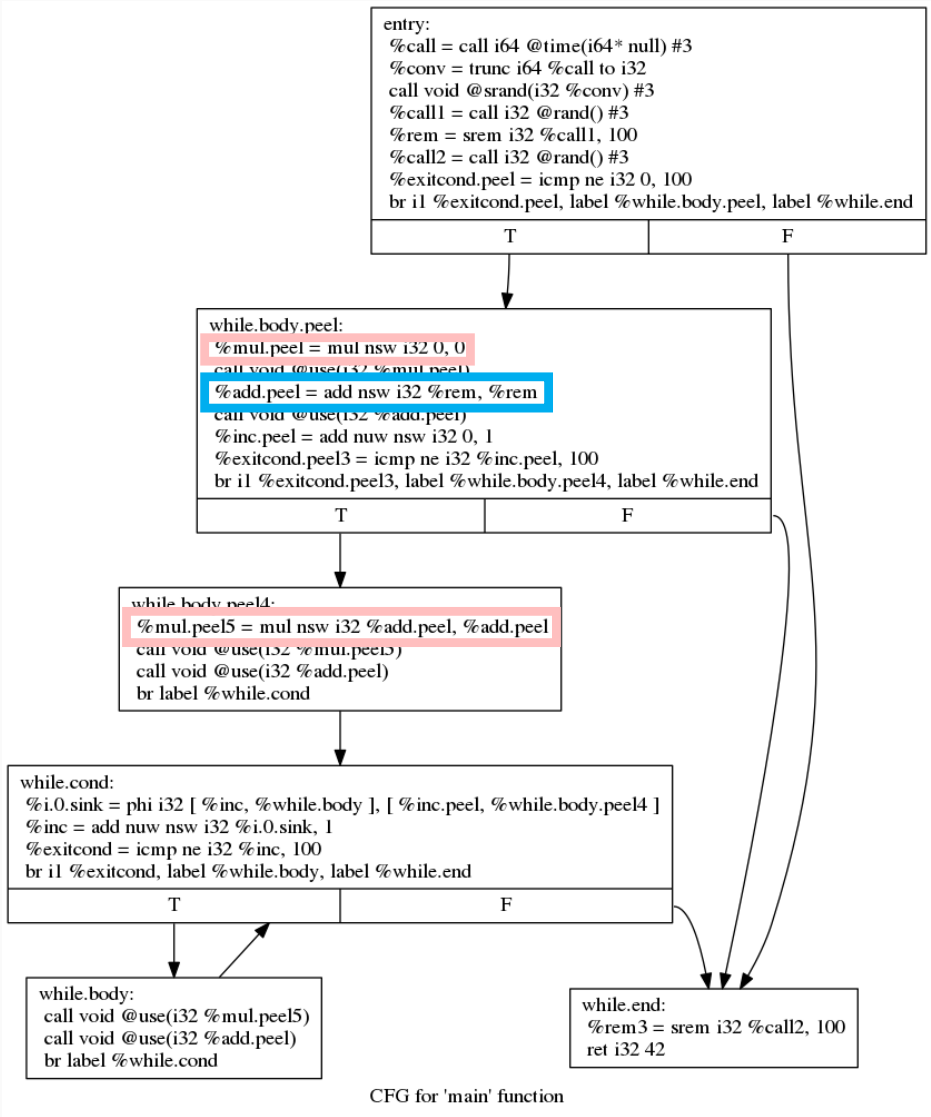
CFG for 'main' function

```
int main(){
    int i=0,y=0;
    srand(time(NULL));
    int x=rand()%100;
    int x2=rand()%100;
    int z;
    if(i<100){
        z=y*y ;
        use(z);
        y=x+x ;
        use(y);
        i=i+1;
    }
    if(i<100){
        z=y*y ;
        use(z);
        use(y);
        i=i+1;
    }
    while(i<100){
        use(z);
        use(y);
        i=i+1;
    }
    return 42;
}
```

Same example



CFG for 'main' function



CFG for 'main' function

Last “relevant” example

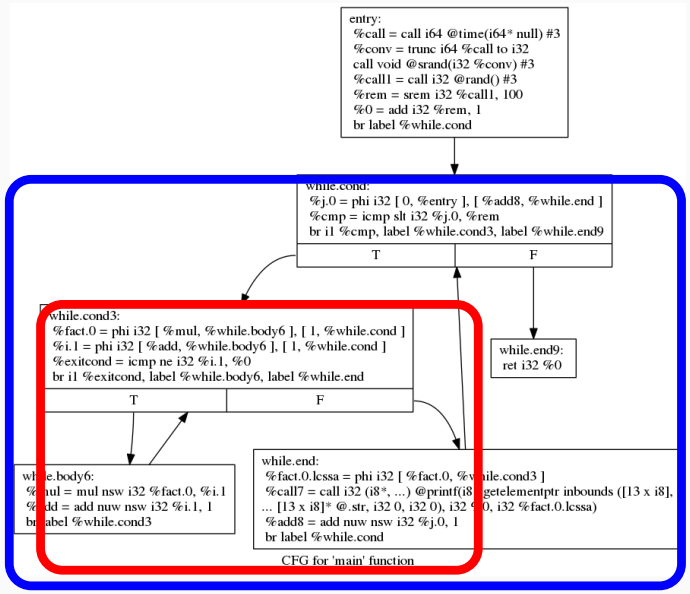
```
while(j<100){
    fact=1;
    I=1;
    while(I<=n){
        fact=fact*I;
        I=I+1;
    }
    use(fact);
    j=j+1;
}
```

Last “relevant” example

```
while(j<100){
    fact=1;
    I=1;
    while(I<=n){
        fact=fact*I;
        I=I+1;
    }
    use(fact);
    j=j+1;
}
```

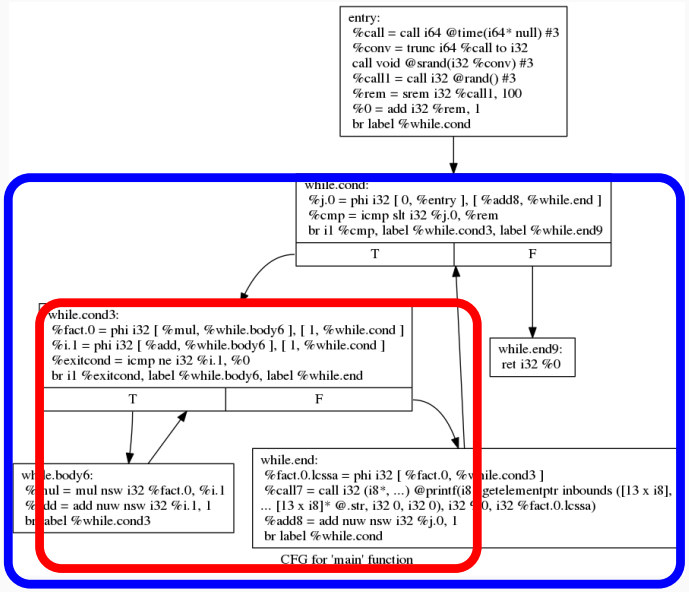
```
if(j<100){
  fact=1;
  I=1;
  while(I<=n){
    fact=fact*I;
    I=I+1;
  }
  use(fact);
  j=j+1;
}
while(j<100){
  use(fact);
  j=j+1;
}
```

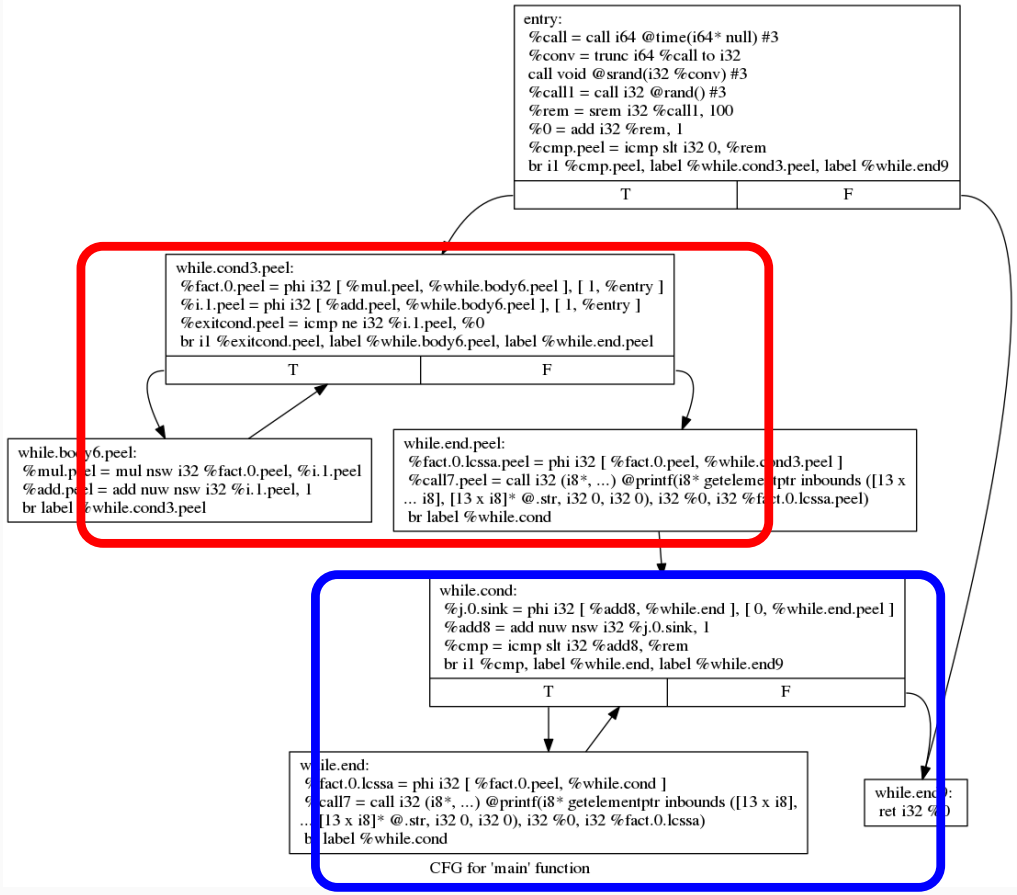
Last “relevant” example




```
if(j<100){
  fact=1;
  I=1;
  while(I<=n){
    fact=fact*I;
    I=I+1;
  }
  use(fact);
  j=j+1;
}
while(j<100){
  use(fact);
  j=j+1;
}
```

Last “relevant” example





LLVM statistics

```
===-------------------------------------------------------------------------===
                          ... Statistics Collected ...
===-------------------------------------------------------------------------===

 2 globalopt        - Number of globals deleted
 5 globalopt        - Number of globals marked unnamed_addr
 1 indvars          - Number of congruent IVs eliminated
 2 indvars          - Number of loop exit tests replaced
 2 indvars          - Number of exit values replaced
10 instcombine      - Number of insts combined
 1 instsimplify     - Number of redundant instructions removed
 8 lcssa            - Number of live out of a loop variables
 1 licm             - Number of instructions hoisted out of loop
 2 loop-rotate      - Number of loops rotated
11 loop-simplify    - Number of pre-header or exit blocks inserted
 2 loop-unswitch    - Total number of instructions analyzed
 1 loop-vectorize   - Number of loops analyzed for vectorization
 2 lqicm            - Number of time runOnLoop is performed...
 3 lqicm            - Number of instructions with deg != -1
 1 lqicm            - Number of innerBlocks with deg != -1
 4 mem2reg          - Number of PHI nodes inserted
 2 mem2reg          - Number of alloca's promoted with a single store
 9 simplifycfg      - Number of blocks simplified
```

LLVM statistics

```
(LQICM Analysis called before each LICM(3X) occurence)
Compiler: clang release_40 -Oz
Time (with - without)
8m8,020s - 7m48,244s
--- vim v8.00442 ---
13407   Number of loop
5009    Loops well analyzed by LQICM
5984    LQICM Quasi-Invariants detected
8126    LICM Invariants Hoisted
656     LQICM Quasi-Invariants blocks detected
7632    LQICM Aborted: several exit blocks
351     LQICM Aborted: Header not exiting
256     LQICM Aborted: Inner loop not analyzed
159     LQICM Aborted: Successor not found
```

Difficulties...

- Apprehend LLVM tools and data structure...
- Consider all strange cases we can have in LLVM-IR
- More and more features have to be implemented to be able to compete with Loop Invariant Code Motion...

Conclusion and further work

We implemented the first skeleton of a huge project
Still a lot of work to do :

- Compile a lot of programs to have more stats
- Make the pass more flexible to take into account more cases
- Start benchmarks with the transformation and compete with LICM !
- Push the compilation community to contribute ?
- mwp-polynomials is an analysis not so far. . .

Conclusion and further work

We implemented the first skeleton of a huge project
Still a lot of work to do :

- Compile a lot of programs to have more stats
- Make the pass more flexible to take into account more cases
- Start benchmarks with the transformation and compete with LICM !
- Push the compilation community to contribute ?
- mwp-polynomials is an analysis not so far. . .
- Helping you to implement your analysis ?