# Advanced modelling techniques

Formal verification, temporal logics, model-checking

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# Objectives of the module

- introduce formal models for critical systems specification
  - automata
  - Petri nets
  - their extensions
- use model-checking to verify their properties
  - reachability
  - deadlocks
  - properties expressed in LTL and CTL logics

### Outline

- Automata
  - Introductory notions
    - Automata
    - Execution and execution tree
    - Atomic properties
  - Formal definitions
    - Automata
    - Behaviour
  - Extensions of automata
    - Automata with variables
    - Synchronised product of automata
    - Synchronisation by message passing
- 3 Model-checking
  - CTL model-checking
  - LTL model-checking

- 2 Temporal logic
  - Language
  - LTL
    - Formal syntax and semantics
    - Illustration
    - Examples of LTL formulae
  - CTL
    - Formal syntax and semantics
    - Illustration
    - Examples of CTL formulae
- Symbolic model-checking
  - Computation of state sets
  - Binary Decision Diagrams
  - Automata representation
- Seachability Properties
  - Reachability in temporal logic
  - Computation of the reachability

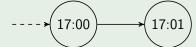
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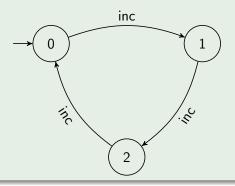


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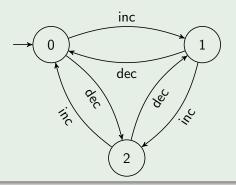




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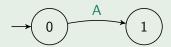


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- code to open door ABA
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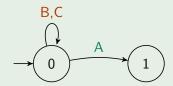
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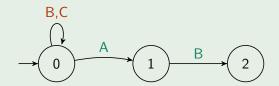
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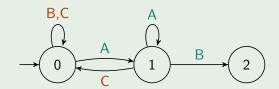
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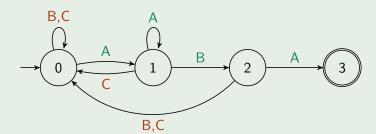
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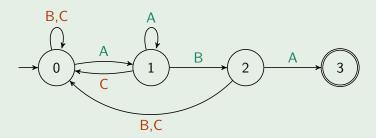


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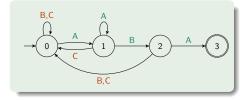


#### Example: Digicode

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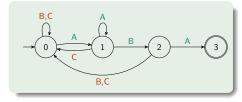


Remark: The numbers in the states are the number of correct keys that have already been pressed.



#### Execution

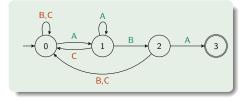
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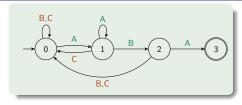
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- Is there a possible infinite execution?



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- All those that end in state 3
- For example 00000000...

#### Execution tree

#### A tree to represent all possible executions

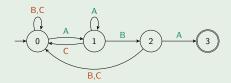
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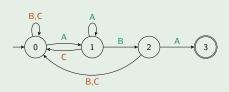


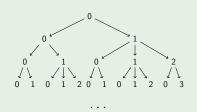
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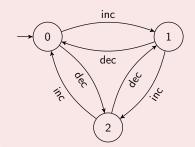
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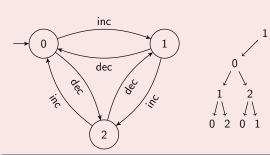
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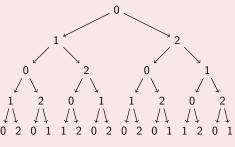
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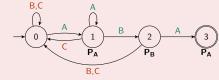
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# Associate properties with states

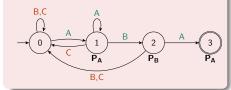


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Prove the correct code was entered when the door opens

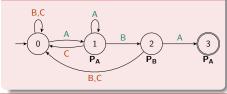
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#### Prove the correct code was entered when the door opens

The door is open only in state 3. Its only predecessor is 2 and transition A is used from state 2 to state 3. So A is the last key pressed.

The only predecessor of 2 is 1, and transition B was used.

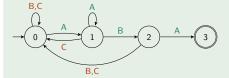
State 1 has two possible predecessors: 0 and 1, and both used *A*. Therefore, the code entered ends with ABA.

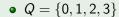
## Formal definition of automata

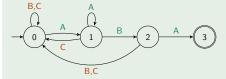
#### Automaton

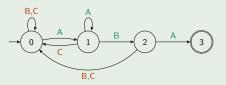
Let *Prop* be a set of atomic propositions. An automaton is a tuple  $\mathcal{A} = \langle Q, E, T, q_0, I, F \rangle$  such that:

- Q is a finite set of states
- E is a finite set of transition labels
- $T \subseteq Q \times E \times Q$  is a set of transitions
- $q_0$  is the initial state
- $I: Q \longrightarrow 2^{Prop}$  associates with each state a finite set of atomic propositions
- F is a set of final states

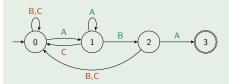




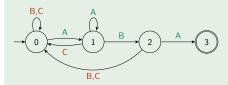




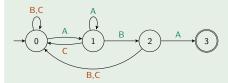
- $Q = \{0, 1, 2, 3\}$
- $E = \{A, B, C\}$



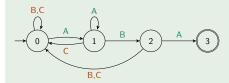
- $Q = \{0, 1, 2, 3\}$
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- $T = \{(0, A, 1), (0, B, 0), (0, C, 0), (1, A, 1), (1, B, 2), (1, C, 0), \}$ (2, A, 3), (2, B, 0), (2, C, 0)



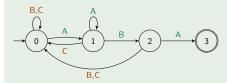
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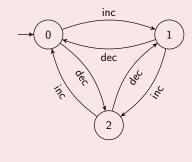


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- $I(0) = \emptyset$ ,  $I(1) = \{P_A\}$ ,  $I(2) = \{P_B\}, I(3) = \{P_A\}$

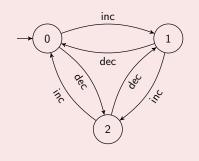


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- *E* = {*inc*, *dec*}
- $\bullet$   $T = \{(0, inc, 1), (0, dec, 2),$ (1, inc, 2), (1, dec, 0),(2, inc, 0), (2, dec, 1),
- $q_0 = 0$
- $Prop = \emptyset$
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- $\bullet$   $F = \emptyset$

# Behaviour

## Runs (or paths)

- A run (or path) of an automaton A is a sequence  $\sigma$  of successive transitions  $(q_i, e_i, q_i')$  of  $\mathcal{A}_i$  i.e. such that  $\forall i, q_{i+1} = q_i'$ .  $\sigma = a_1 \xrightarrow{e_1} a_2 \xrightarrow{e_2} a_3 \xrightarrow{e_3} a_4 \dots$
- The length of a run  $\sigma$  is its number of transitions  $|\sigma| \in \mathbb{N} \cup \{\omega\}$ where  $\omega$  denotes infinity.
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#### Executions

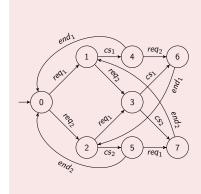
- A partial execution of A is a run starting from the initial state  $q_0$ .
- A complete execution of  $\mathcal{A}$  is an execution that is maximal. It is either infinite or ends in a state where no transition is possible. This state might be final (in F), or a deadlock.
- A state is reachable if there exists an execution in which it appears.
- The complete executions define the behaviour of the automaton.

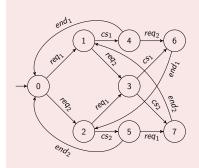
#### Mutual exclusion between two processes

- two processes execute and need access to the same resource
- each process can request access to a critical section of its code
- they must not execute this part at the same time
- when they have finished they signal they exit their critical section and loop back to their initial state

#### Questions

- Model this problem with an automaton
- Associate atomic properties with each state
- Is the mutual exclusion requirement satisfied?
- Is the system fair?
- What would happen if you wanted to add a third process?



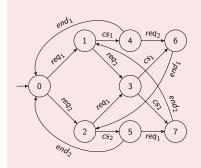


 $\bigcirc$   $P_i$ : Process *i* is requesting access,  $C_i$ : Process *i* is in its critical section,  $R_i$ : Process *i* is at rest.

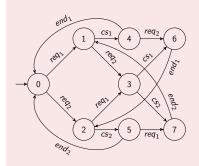
 $P_1$ : states 1, 3, 7;  $P_2$ : states 2, 3, 6;

 $C_1$ : states 4, 6;  $C_2$ : states 5, 7;

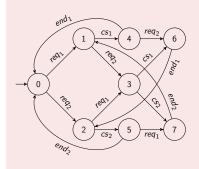
 $R_1$ : states 0, 2, 5;  $R_2$ : states 0, 1, 4



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- **3** Yes: no state has both  $C_1$  and  $C_2$
- No: run 0137137...never allows process 1 to enter its critical section
- The number of states would blow up

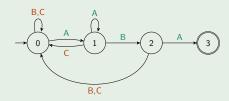
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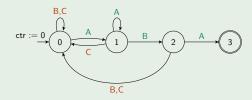
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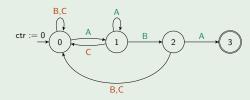


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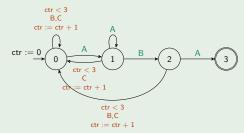


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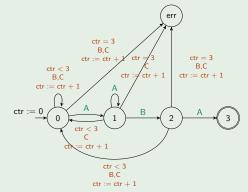




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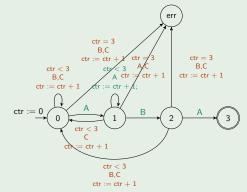
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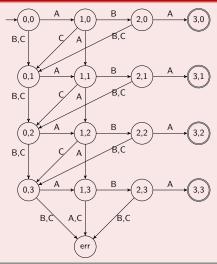
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Exercise: The digicode with 3 errors without variables

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# Synchronised product

## Why?

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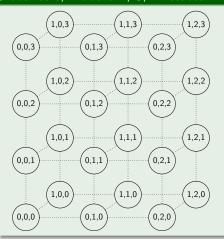
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- composition of automata

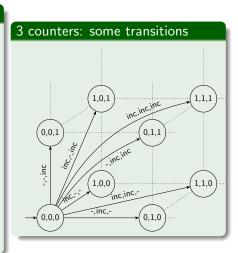
#### How?

- independent actions lead to a cartesian product of states
- synchronised actions occur simultaneously

# Synchronised product

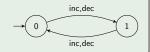




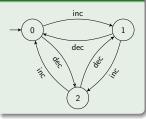


# Example: Synchronised counters

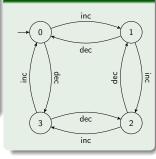
#### Modulo 2 counter



## Modulo 3 counter

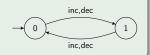


## Modulo 4 counter

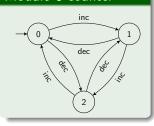


# Example: Synchronised counters

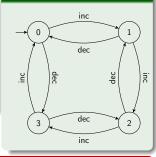
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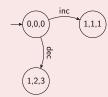
### Modulo 3 counter



## Modulo 4 counter

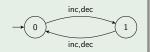


# Synchronised actions: all counters increment or decrement simultaneously

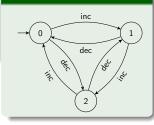


# Example: Synchronised counters

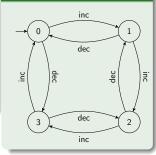
#### Modulo 2 counter



### Modulo 3 counter



## Modulo 4 counter



Synchronised actions: all counters increment or decrement simultaneously

# Formal definition of the cartesian product

Let  $(A_i)_{1 \leq i \leq n}$  be a family of automata  $A_i = \langle Q_i, E_i, T_i, q_{0i}, I_i, F_i \rangle$ .

#### Cartesian product of automata

The cartesian product  $A_1 \times \cdots \times A_n$  of the automata in the family is the automaton  $A = \langle Q, E, T, q_0, I, F \rangle$  such that :

- $Q = Q_1 \times \cdots \times Q_n$
- $E = \prod_{1 \le i \le n} (E_i \cup \{-\})$  (where represents an empty action)
- $T = \{((q_1, \ldots, q_n), (e_1, \ldots, e_n), (q'_1, \ldots, q'_n)) \mid \\ \forall 1 \leq i \leq n, (e_i = \land q'_i = q_i) \lor (e_i \neq \land (q_i, e_i, q'_i) \in T_i)\}$
- $q_0 = (q_{01}, \ldots, q_{0n})$
- $\forall (q_1,\ldots,q_n) \in Q : l((q_1,\ldots,q_n)) = \bigcup_{1 \leq i \leq n} l_i(q_i)$
- $F = \{(q_1, \ldots, q_n) \in Q \mid \exists 1 \leq i \leq n, q_i \in F_i\}$

### Formal definition of the synchronised product

Let  $(A_i)_{1 \le i \le n}$  be a family of automata  $A_i = \langle Q_i, E_i, T_i, q_{0i}, I_i, F_i \rangle$ .

#### Synchronisation set

The synchronisation set, denoted *Sync* describes all permitted simultaneous actions:

$$Sync \subseteq \prod_{1 \le i \le n} (E_i \cup \{-\})$$

## Formal definition of the synchronised product

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#### Synchronisation set

The synchronisation set, denoted *Sync* describes all permitted simultaneous actions:

$$Sync \subseteq \prod_{1 < i < n} (E_i \cup \{-\})$$

### Synchronised product of automata

The synchronised product of  $(A_i)_{1 \le i \le n}$  over a set Sync is the cartesian product restricted to E = Sync.

# Synchronisation by message passing

### Message passing: a special case of synchronised product

- !m send a message m
- ?m receive a message m
- reception and sending occur simultaneously
- they concern the same message

## Synchronisation by message passing

### Example: a small lift

Model of a lift in a 3 floors building, composed of:

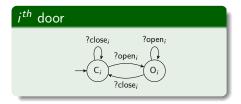
the cabin which goes up and down according to the current floor and the lift controller commands

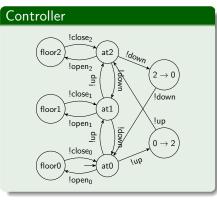
3 doors (one per floor) which open and close according to the controller's commands

a controller which operates the lift

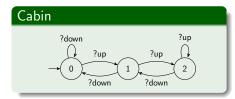
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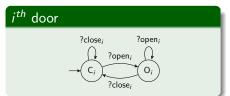
#### Cabin ?up ?down ?up ?up 0 2 ?down ?down

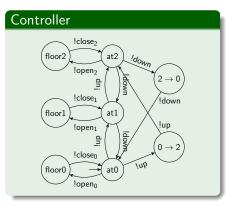




## Example: the lift





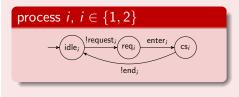


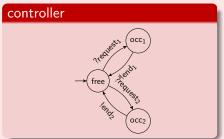
### **Properties**

- A door on a floor cannot open while the cabin is on a different floor
- The cabin cannot move while one of the doors is open

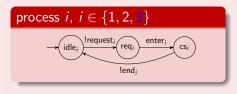
- Model the mutual exclusion problem with message passing:
  - one automaton per participating process (2 processes)
  - a controller
- 2 How do you add a new process? Give the model for 3 processes, and explain how to generalise it to *n* processes

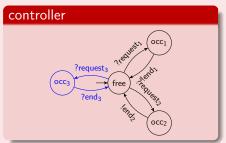
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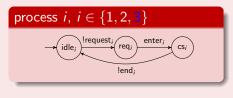


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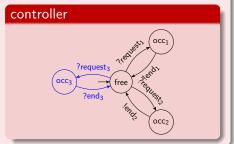




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- n process automata
- controller: n states occ



### Outline

- 2 Temporal logic
  - Language
  - LTL
    - Formal syntax and semantics
    - Illustration
    - Examples of LTL formulae
  - CTL
    - Formal syntax and semantics
    - Illustration
    - Examples of CTL formulae

- express dynamic behaviour of the system
- use formal syntax and semantics to avoid any ambiguity
- capture statements and reasoning that involve the notion of order in time

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any request must ultimately be satisfied

 the lift never traverses a floor for which a request is pending without satisfying the request

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- any request must ultimately be satisfied False: The lift can continuously go up and down without opening doors (run (at0,C<sub>0</sub>,C<sub>1</sub>,C<sub>2</sub>,0) $\xrightarrow{up}$ (at1,C<sub>0</sub>,C<sub>1</sub>,C<sub>2</sub>,1) $\xrightarrow{up}$  $(at2,C_0,C_1,C_2,2) \xrightarrow{down} (at1,C_0,C_1,C_2,1)...)$
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- the lift never traverses a floor for which a request is pending without satisfying the request
  - False: consequence of the previous property

# The language CTL\*

- atomic propositions
- boolean combinators:
  - true, false
  - ¬ (negation)
  - ∧ (and), ∨ (or)
  - ullet  $\Longrightarrow$  (logical implication),  $\Longleftrightarrow$  (if and only if)
- temporal combinators:
  - X (neXt), F (Future), G (Globally)
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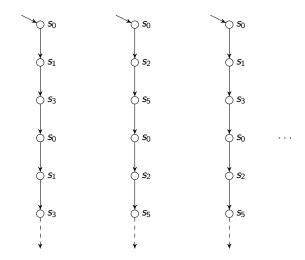
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### Main temporal logics

LTL Linear-time Temporal Logic

CTL Computation Tree Logic

# LTL: Linear-time Temporal Logic



### Semantics of LTL

Let  $\sigma$  be a run and  $p \in Prop$  an atomic proposition.  $\sigma, i \models \phi$  denotes that at time i of its execution,  $\sigma$  satisfies formula  $\phi$ .

### Illustration of the LTL semantics

 Χφ  $\bullet$  F $\phi$  $\bullet$   $G\phi$  $\bullet$   $\phi_1 U \phi_2$ 

- What do the following formulae mean?
- Which runs satisfy the LTL property?

#### Modulo 3 counter

XXX0

**2**  $F(1 \lor 2)$ 

**6** F1

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The third state reached is 0

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  - The third state reached is 0 All runs starting with 0120 or 0210
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### The digicode

F3

**2** G¬3

- What do the following formulae mean?
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  The door can open
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- F3
  - The door can open All runs ending in state 3
- G¬3 The door never opens All runs not ending in state 3

Laure Petrucci

Express  $\vee$ ,  $\Longrightarrow$ ,  $\Longleftrightarrow$ , W with  $\neg$ ,  $\wedge$ , X, F, G, U

(W is similar to U but  $\psi$  may never happen)

### Express $\vee$ , $\Longrightarrow$ , $\Longleftrightarrow$ , W with $\neg$ , $\wedge$ , X, F, G, U

(W is similar to U but  $\psi$  may never happen)

- $\phi \lor \psi \equiv \neg (\phi \land \psi)$
- $\phi \iff \psi \equiv (\neg \phi \lor \psi) \land (\phi \lor \neg \psi)$
- $\phi W \psi \equiv (\phi U \psi) \vee G \phi$

### Prove that:

•  $F\phi \equiv trueU\phi$ 

 $\mathbf{Q} \quad \mathsf{G}\phi \equiv \neg \mathsf{F} \neg \phi$ 

#### Prove that:

- $\mathsf{F}\phi \equiv \mathsf{true}\mathsf{U}\phi$  $\mathsf{true} \mathsf{U} \phi \equiv \exists j, i \leq j \leq |\sigma| : \sigma, j \models \phi \land \forall k, i \leq k < j : \sigma, k \models \mathsf{true}$  $\equiv \exists j, i \leq j \leq |\sigma| : \sigma, j \models \phi$  $\equiv \mathsf{F}\phi$
- $\bullet$   $\mathsf{G}\phi \equiv \neg \mathsf{F} \neg \phi$

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 $\bullet$   $\mathsf{G}\phi \equiv \neg \mathsf{F} \neg \phi$  $\neg \mathsf{F} \neg \phi \equiv \neg (\exists j, j < j < |\sigma| : \sigma, j \models \neg \phi)$  $\equiv \forall j, j < j < |\sigma| : \sigma, j \not\models \neg \phi$  $\equiv \forall i, i < j < |\sigma| : \sigma, i \models \phi$  $\equiv \mathsf{G}\phi$ 

- Write a LTL formula satisfied by all runs where keys A and B have successively been pressed
- Write a LTL formula that characterises the infinite loop on state 0
- 3 Same question using atomic propositions  $P_A$ ,  $P_B$ ,  $P_C$

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- Same question using atomic propositions  $P_A$ ,  $P_B$ ,  $P_C$  $G \neg P_{\Delta}$

### Mutual exclusion between two processes (synchronised product)

Write an LTL formula satisfied by all runs where:

- 1 The two processes are not simultaneously in their critical section
- Whenever process 1 requests to enter its critical section, it will eventually succeed

### Mutual exclusion between two processes (synchronised product)

Write an LTL formula satisfied by all runs where:

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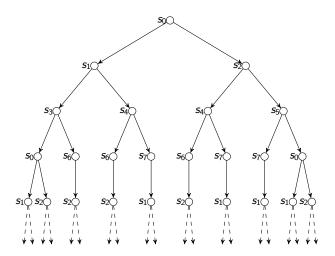
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$$G(req_1 \implies Fcs_1)$$

# CTL: Computation Tree Logic



### Semantics of CTL

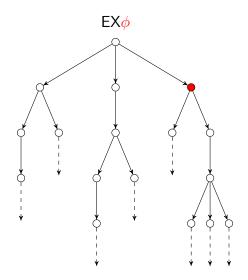
#### Same as LTL plus:

$$\sigma, i \models \mathsf{E}\phi \quad \mathsf{iff} \quad \exists \sigma' : \sigma(0) \dots \sigma(i) = \sigma'(0) \dots \sigma'(i) \text{ and } \sigma', i \models \phi \\ \sigma, i \models \mathsf{A}\phi \quad \mathsf{iff} \quad \forall \sigma' : \sigma(0) \dots \sigma(i) = \sigma'(0) \dots \sigma'(i) \text{ we have } \sigma', i \models \phi$$

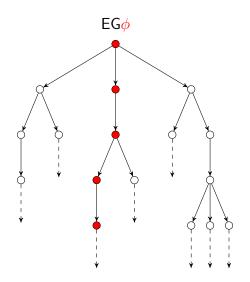
In CTL, each use of a temporal operator (X, F, G, U) is in the immediate scope of a quantifier (E, A)

This restriction does not apply in CTL\*

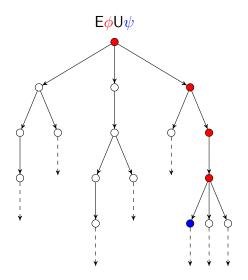
# Illustration of the CTL semantics (1/8)



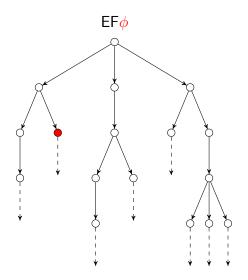
# Illustration of the CTL semantics (2/8)



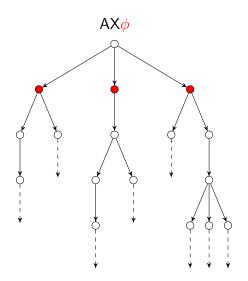
# Illustration of the CTL semantics (3/8)



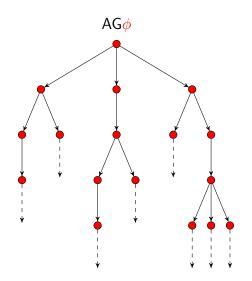
# Illustration of the CTL semantics (4/8)



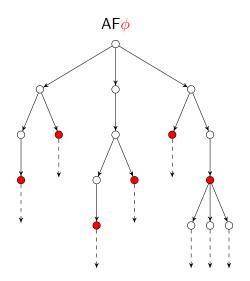
# Illustration of the CTL semantics (5/8)



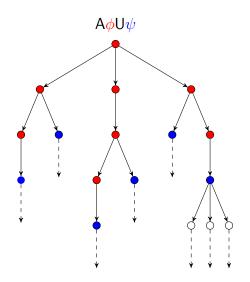
# Illustration of the CTL semantics (6/8)



# Illustration of the CTL semantics (7/8)



# Illustration of the CTL semantics (8/8)



Explain the following CTL formulae, and if they are true or false:

### Mutual exclusion between 2 processes (synchronised product)

 $\bigcirc$  AG $\neg$ ( $cs_1 \land cs_2$ )

 $\bigcirc$  AG( $reg_1 \implies AFcs_1$ )

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  - A.C.(
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     It is always the case that when process 1 requests access to its critical section, it will eventually be granted false
  - AG(EF(idle<sub>1</sub> ∧ idle<sub>2</sub>))
    Whatever the state of the system, it is possible to have both processes idle in the future.

    true

#### Prove that:

- EF $\phi \equiv \text{EtrueU}\phi$
- $\bigcirc$  AX $\phi \equiv \neg EX \neg \phi$

**3** AG $\phi \equiv \neg (\text{EtrueU} \neg \phi)$ 

 $\bullet$  AF $\phi \equiv \neg EG \neg \phi$ 

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- $\bullet$  EF $\phi \equiv \text{EtrueU}\phi$ We already proved that  $F\phi \equiv trueU\phi$ . Hence:  $EF\phi \equiv E(trueU\phi)$
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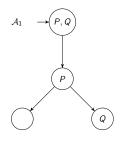
#### Prove that:

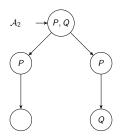
- EF $\phi \equiv \text{EtrueU}\phi$ We already proved that F $\phi \equiv \text{trueU}\phi$ . Hence: EF $\phi \equiv \text{E}(\text{trueU}\phi)$
- ③  $\mathsf{AG}\phi \equiv \neg(\mathsf{EtrueU}\neg\phi)$ We know that  $\mathsf{EF}\phi \equiv \mathsf{EtrueU}\phi$  and  $\mathsf{G}\phi \equiv \neg\mathsf{F}\neg\phi$ . Hence:  $\neg(\mathsf{EtrueU}\neg\phi) \equiv \neg\mathsf{EF}\neg\phi$  $\equiv \mathsf{A}\neg\mathsf{F}\neg\phi$  $\equiv \mathsf{AG}\phi$

#### Prove that:

- $\bullet$  EF $\phi \equiv \text{EtrueU}\phi$ We already proved that  $F\phi \equiv trueU\phi$ . Hence:  $EF\phi \equiv E(trueU\phi)$
- $\bigcirc$  AX $\phi \equiv \neg EX \neg \phi$  $\neg \mathsf{EX} \neg \phi \equiv \neg (\exists \sigma' : \sigma(0) \dots \sigma(i) = \sigma'(0) \dots \sigma'(i) \land \sigma', i \models \mathsf{X} \neg \phi)$  $\equiv \forall \sigma' : \sigma(0) \dots \sigma(i) = \sigma'(0) \dots \sigma'(i)$  we have  $\sigma', i \not\models X \neg \phi$  $\equiv \forall \sigma' : \sigma(0) \dots \sigma(i) = \sigma'(0) \dots \sigma'(i)$  we have  $\sigma', i \models X\phi$  $\equiv AX\phi$
- $\bullet$  AG $\phi \equiv \neg (\text{EtrueU} \neg \phi)$ We know that  $\mathsf{EF}\phi \equiv \mathsf{Etrue}\mathsf{U}\phi$  and  $\mathsf{G}\phi \equiv \neg\mathsf{F}\neg\phi$ . Hence:  $\neg(\mathsf{EtrueU}\neg\phi) \equiv \neg\mathsf{EF}\neg\phi$  $\equiv A \neg F \neg \phi$  $\equiv AG\phi$
- $\bullet$  AF $\phi \equiv \neg EG \neg \phi$  $\neg \mathsf{EG} \neg \phi \equiv \mathsf{A} \neg \mathsf{G} \neg \phi$  $\equiv AF\phi$

### LTL and CTL do not recognise the same behaviours





#### LTL

Runs for both automata:

- $\{P,Q\} \{P\} \{-\}$
- $\{P,Q\}\{P\}\{Q\}$

 $\forall \phi : \mathcal{A}_1 \models \phi \iff \mathcal{A}_2 \models \phi$ 

#### **CTL**

$$A_1 \models \mathsf{AX}(\mathsf{EX}Q \land \mathsf{EX}\neg Q)$$

 $A_2 \not\models \mathsf{AX}(\mathsf{EX}Q \land \mathsf{EX} \neg Q)$ 

### Outline

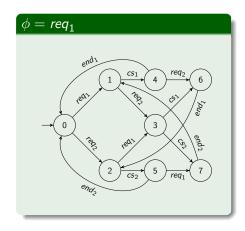
- Model-checking
  - CTL model-checking
  - LTL model-checking

## CTL model-checking algorithm

- algorithm marking states where a formula is satisfied
- memorises the already computed results
- reuses the computed results of sub-formulae to compute new formulae

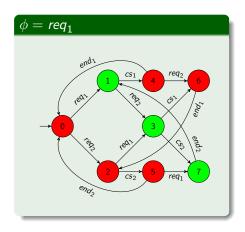
# CTL model-checking algorithm

### **Procedure** marking( $\phi$ )



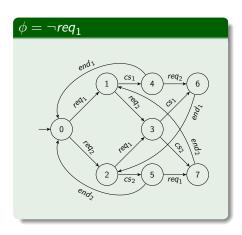
# CTL model-checking algorithm

### **Procedure** marking( $\phi$ )



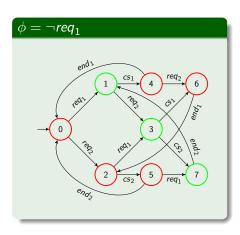
# Case 2: $\phi = \neg \psi$

marking( $\psi$ ); forall  $q \in Q$  do  $| \mathbf{q}.\phi := \neg \mathbf{q}.\psi$ 



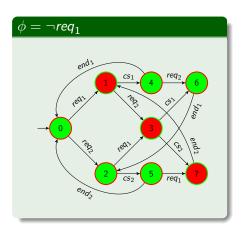
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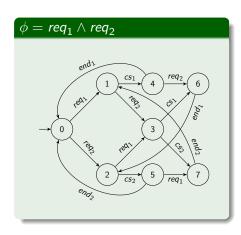
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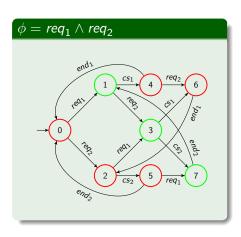
# Case 3: $\phi = \psi_1 \wedge \psi_2$

 $\begin{aligned} & \mathsf{marking}(\psi_1); \\ & \mathsf{marking}(\psi_2); \\ & \mathbf{forall} \ \ q \in Q \ \mathbf{do} \\ & | \ \ \mathsf{q}.\phi {:=} \mathsf{q}.\psi_1 {\wedge} \mathsf{q}.\psi_2 \end{aligned}$ 



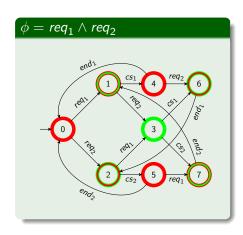
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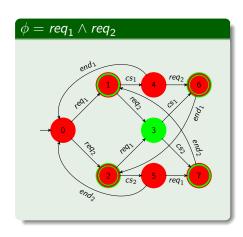
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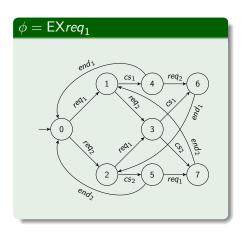
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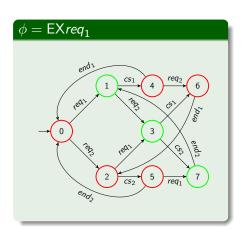


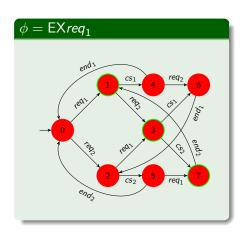
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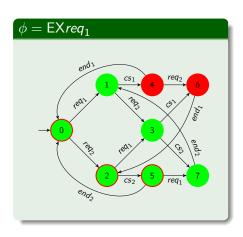
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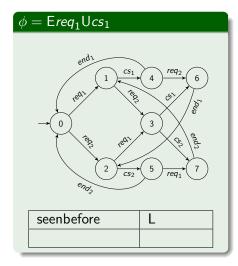




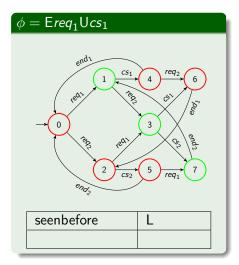




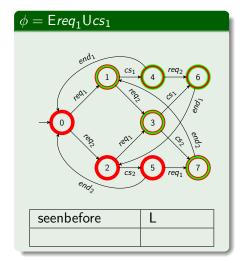
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forall q \in Q do
    q.\phi:=false;
    g.seenbefore:=false
L:=0:
forall q \in Q do
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     L:=L\cup\{q\}
while L \neq \emptyset do
    pick q from L; L:=L\\{q\};
    q.\phi:=true;
    forall (q', \_, q) \in T do
         if q'.seenbefore=false then
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              if q'.\psi_1=true then
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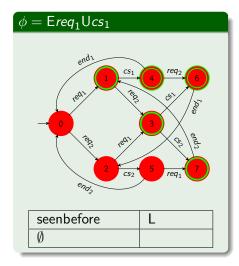
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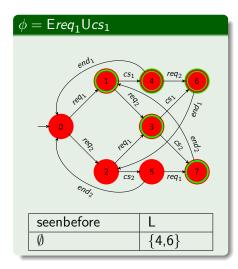
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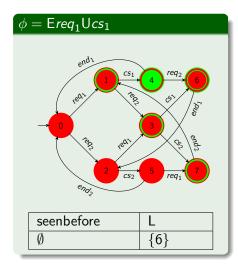
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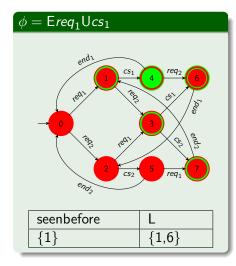
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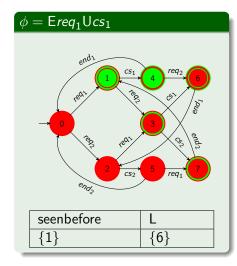
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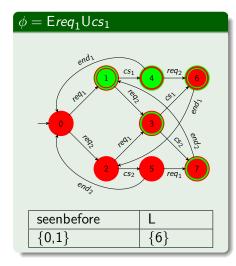
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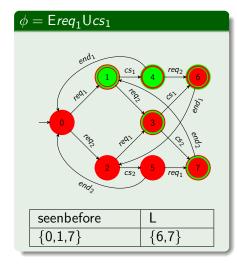
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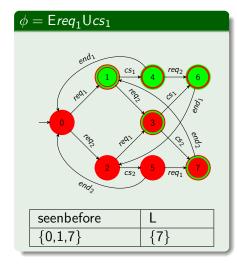
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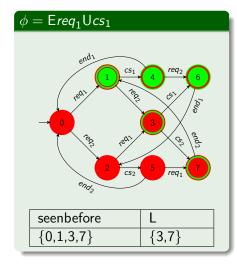
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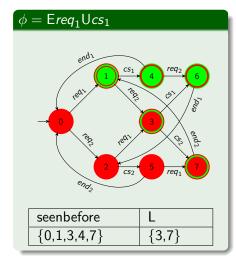
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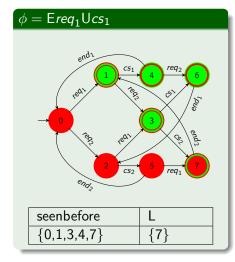
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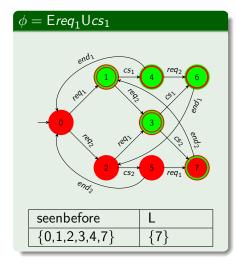
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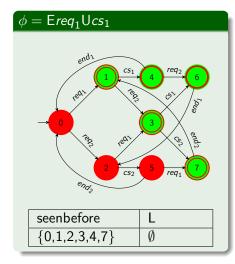
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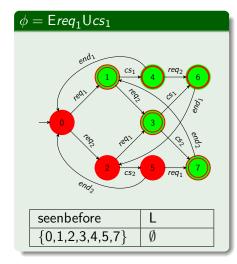
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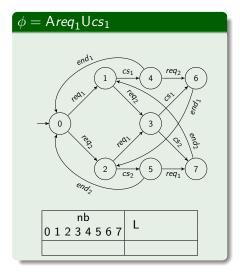
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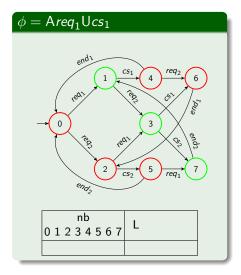
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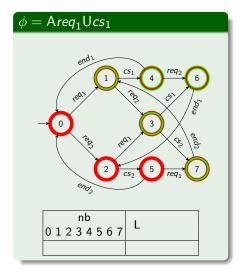
```
marking(\psi_1);
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L:=Ø:
forall q \in Q do
    q.nb:=degree(q);
   q.\phi:=false
forall q \in Q do
    if q.\psi_2=true then
     L:=L\cup\{q\}
while L \neq \emptyset do
    pick q from L; L:=L\\{q\};
    q.\phi:=true;
    forall (q', \_, q) \in T do
         q'.nb:=q'.nb - 1;
         if q'.nb=0 and q'.\psi_1=true
         and q'.\phi=false then
           | L:=L∪ {q'}
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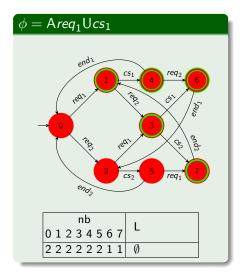
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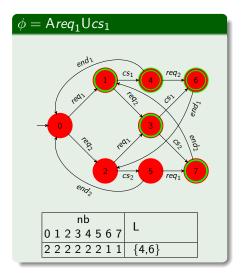
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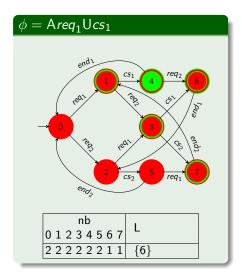
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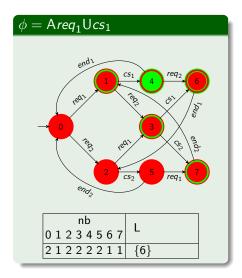
```
marking(\psi_1);
marking(\psi_2);
L:=Ø:
forall q \in Q do
    q.nb:=degree(q);
    q.\phi:=false
forall q \in Q do
    if q.\psi_2=true then
     L:=L\cup\{q\}
while L \neq \emptyset do
    pick q from L; L:=L\\{q\};
    q.\phi:=true;
    forall (q', \_, q) \in T do
         q'.nb:=q'.nb-1;
         if q'.nb=0 and q'.\psi_1=true
         and q'.\phi=false then
           | L:=L∪ {q'}
```



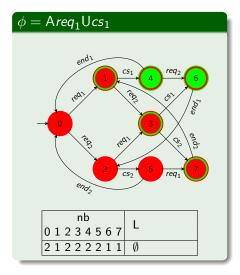
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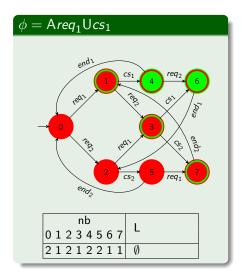
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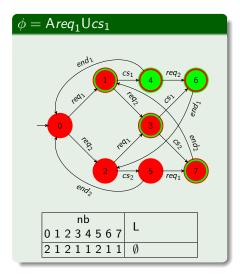
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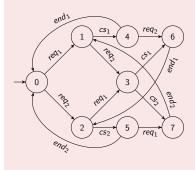


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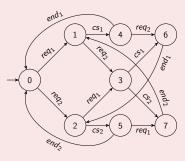
#### **Exercises**

#### Check $\overline{\mathsf{AG}(\mathsf{EF}(idle_1 \wedge idle_2))}$



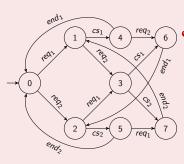
#### **Exercises**

#### Check $AG(EF(idle_1 \wedge idle_2))$



```
\begin{array}{l} \mathsf{AG}(\mathsf{EF}(\mathit{idle}_1 \wedge \mathit{idle}_2)) \\ \bullet & \equiv \neg(\mathsf{EtrueU} \neg(\mathsf{EF}(\mathit{idle}_1 \wedge \mathit{idle}_2))) \\ & \equiv \neg(\mathsf{EtrueU} \neg(\mathsf{E}(\mathsf{trueU}(\mathit{idle}_1 \wedge \mathit{idle}_2)))) \end{array}
```

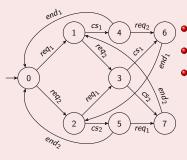
# Check $AG(EF(idle_1 \wedge idle_2))$



 $AG(EF(idle_1 \land idle_2))$ 

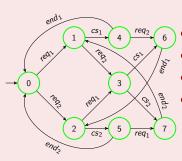
- $\equiv \neg(\mathsf{EtrueU} \neg(\mathsf{EF}(\mathit{idle}_1 \land \mathit{idle}_2)))$  $\equiv \neg(\mathsf{EtrueU} \neg(\mathsf{E}(\mathsf{trueU}(\mathit{idle}_1 \land \mathit{idle}_2))))$
- mark idle<sub>1</sub>: states 0, 2, 5

# Check $AG(EF(idle_1 \wedge idle_2))$



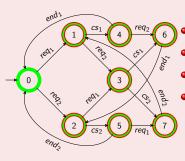
- $AG(EF(idle_1 \wedge idle_2))$
- $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{EF}(\mathit{idle}_1 \land \mathit{idle}_2)))$  $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{E}(\mathsf{trueU}(\mathit{idle}_1 \land \mathit{idle}_2))))$
- mark idle<sub>1</sub>: states 0, 2, 5
- mark idle<sub>2</sub>: states 0, 1, 4
- case 3, mark idle<sub>1</sub> ∧ idle<sub>2</sub>: state 0

# Check $AG(EF(idle_1 \land idle_2))$



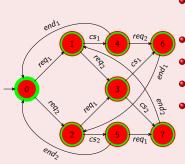
 $AG(EF(idle_1 \wedge idle_2))$ 

- $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{EF}(idle_1 \land idle_2)))$  $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{E}(\mathsf{trueU}(idle_1 \land idle_2))))$
- mark idle<sub>1</sub>: states 0, 2, 5
- mark idle<sub>2</sub>: states 0, 1, 4
- case 3, mark idle<sub>1</sub> ∧ idle<sub>2</sub>: state 0
- case 5, mark  $\phi_1 = \mathsf{E}(\mathsf{trueU}(idle_1 \wedge idle_2))$

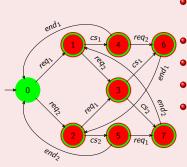


- $AG(EF(idle_1 \wedge idle_2))$
- $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{EF}(idle_1 \land idle_2)))$  $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{E}(\mathsf{trueU}(idle_1 \land idle_2))))$
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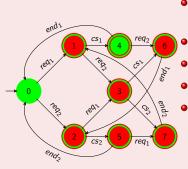
## Check $AG(EF(idle_1 \wedge idle_2))$



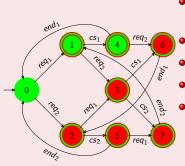
- $\mathsf{AG}(\mathsf{EF}(\mathit{idle}_1 \wedge \mathit{idle}_2))$
- $\equiv \neg(\mathsf{EtrueU} \neg(\mathsf{EF}(\mathit{idle}_1 \land \mathit{idle}_2)))$  $\equiv \neg(\mathsf{EtrueU} \neg(\mathsf{E}(\mathsf{trueU}(\mathit{idle}_1 \land \mathit{idle}_2))))$
- mark idle<sub>1</sub>: states 0, 2, 5
- mark idle<sub>2</sub>: states 0, 1, 4
- case 3, mark  $idle_1 \wedge idle_2$ : state 0
- case 5, mark  $\phi_1 = \mathsf{E}(\mathsf{trueU}(\mathit{idle}_1 \land \mathit{idle}_2))$



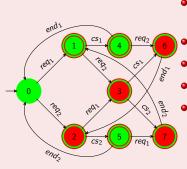
- $AG(EF(idle_1 \wedge idle_2))$
- $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{EF}(idle_1 \land idle_2)))$  $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{E}(\mathsf{trueU}(idle_1 \land idle_2))))$
- mark idle<sub>1</sub>: states 0, 2, 5
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- case 3, mark idle<sub>1</sub> ∧ idle<sub>2</sub>: state 0
- case 5, mark  $\phi_1 = \mathsf{E}(\mathsf{trueU}(idle_1 \wedge idle_2))$



- $AG(EF(idle_1 \wedge idle_2))$
- $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{EF}(idle_1 \land idle_2)))$  $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{E}(\mathsf{trueU}(idle_1 \land idle_2))))$
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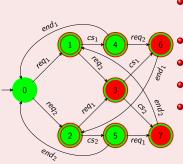


- $AG(EF(idle_1 \wedge idle_2))$
- $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{EF}(idle_1 \land idle_2)))$  $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{E}(\mathsf{trueU}(idle_1 \land idle_2))))$
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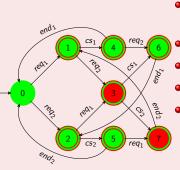
- $AG(EF(idle_1 \wedge idle_2))$
- $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{EF}(idle_1 \land idle_2)))$  $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{E}(\mathsf{trueU}(idle_1 \land idle_2))))$
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## Check $AG(EF(idle_1 \land idle_2))$



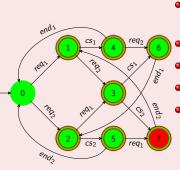
 $AG(EF(idle_1 \wedge idle_2))$ 

- $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{EF}(idle_1 \land idle_2)))$  $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{E}(\mathsf{trueU}(idle_1 \land idle_2))))$
- mark idle<sub>1</sub>: states 0, 2, 5
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- case 5, mark  $\phi_1 = \mathsf{E}(\mathsf{trueU}(idle_1 \wedge idle_2))$

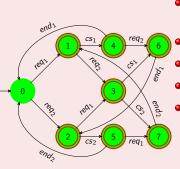


- $AG(EF(idle_1 \wedge idle_2))$
- $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{EF}(idle_1 \land idle_2)))$  $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{E}(\mathsf{trueU}(idle_1 \land idle_2))))$
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- mark idle<sub>2</sub>: states 0, 1, 4
- case 3, mark idle<sub>1</sub> ∧ idle<sub>2</sub>: state 0
- case 5, mark  $\phi_1 = \mathsf{E}(\mathsf{trueU}(idle_1 \wedge idle_2))$

## Check $AG(EF(idle_1 \wedge idle_2))$

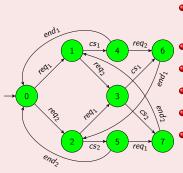


- $\mathsf{AG}(\mathsf{EF}(\mathit{idle}_1 \wedge \mathit{idle}_2))$
- $\equiv \neg(\mathsf{EtrueU}\neg(\mathsf{EF}(\mathit{idle}_1 \land \mathit{idle}_2)))$  $\equiv \neg(\mathsf{EtrueU}\neg(\mathsf{E}(\mathsf{trueU}(\mathit{idle}_1 \land \mathit{idle}_2))))$
- mark idle<sub>1</sub>: states 0, 2, 5
- mark idle2: states 0, 1, 4
- case 3, mark  $idle_1 \wedge idle_2$ : state 0
- case 5, mark  $\phi_1 = \mathsf{E}(\mathsf{trueU}(\mathit{idle}_1 \land \mathit{idle}_2))$



- $AG(EF(idle_1 \wedge idle_2))$
- $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{EF}(idle_1 \land idle_2)))$  $\equiv \neg (\mathsf{EtrueU} \neg (\mathsf{E}(\mathsf{trueU}(idle_1 \land idle_2))))$
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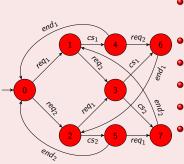
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 $AG(EF(idle_1 \wedge idle_2))$ 

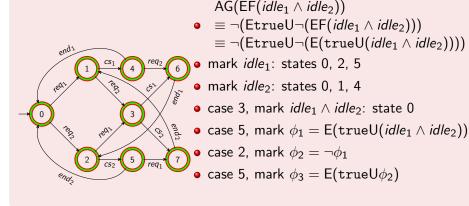
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## Check $AG(EF(idle_1 \wedge idle_2))$

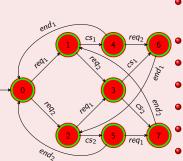


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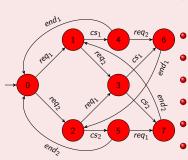
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 $\mathsf{AG}(\mathsf{EF}(\mathit{idle}_1 \wedge \mathit{idle}_2))$ 

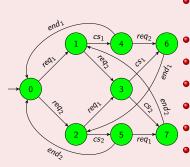
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- case 5, mark  $\phi_3 = \mathsf{E}(\mathtt{true}\mathsf{U}\phi_2)$

## Check $AG(EF(idle_1 \wedge idle_2))$



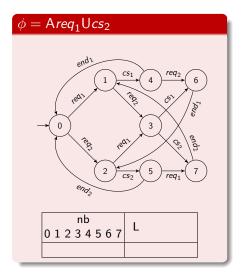
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- case 2, mark  $\phi_2 = \neg \phi_1$
- case 5, mark  $\phi_3 = \mathsf{E}(\mathtt{true}\mathsf{U}\phi_2)$
- case 2, mark  $\phi_4 = \neg \phi_3$

## Check $AG(EF(idle_1 \wedge idle_2))$

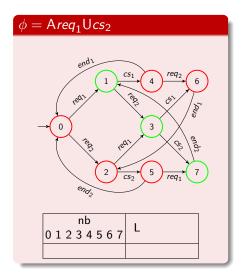


- $AG(EF(idle_1 \wedge idle_2))$
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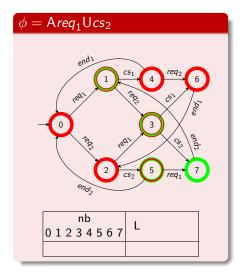
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         q'.nb:=q'.nb - 1;
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           | L:=L∪ {q'}
```



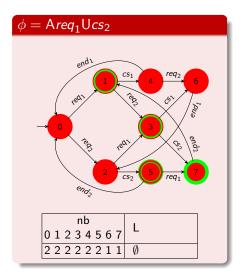
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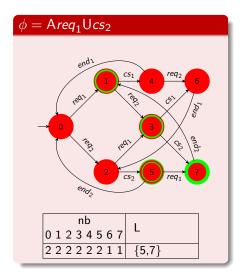
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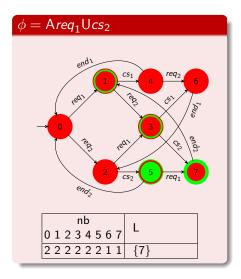
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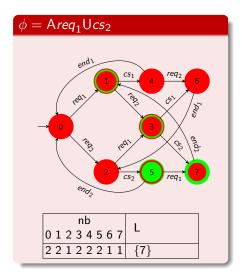
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marking(\psi_2);
L:=\emptyset:
forall q \in Q do
    q.nb:=degree(q);
    q.\phi:=false
forall q \in Q do
    if q.\psi_2=true then
      | L:=L\cup\{q\}
while L \neq \emptyset do
     pick q from L; L:=L\\{q\};
    q.\phi:=true;
    forall (q', \_, q) \in T do
          q'.nb:=q'.nb-1;
          if q'.nb=0 and q'.\psi_1=true
          and q'.\phi=false then
           | L:=L∪ {q'}
```



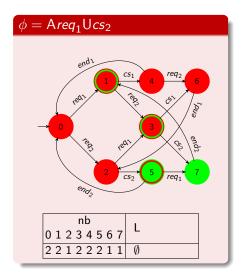
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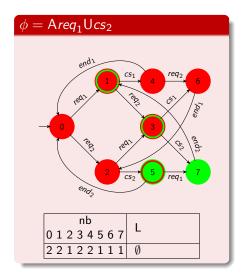
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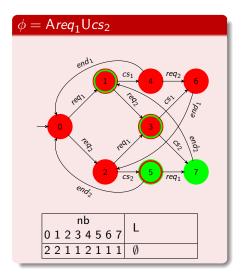
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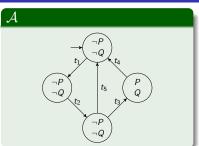


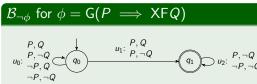
# LTL model-checking

#### Algorithm working on path formulae

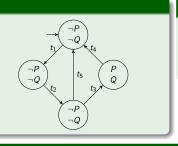
## Principle for checking if $A \models \phi$

- lacktriangledown construct automaton  $\mathcal{B}_{\neg\phi}$  recognising all executions not satisfying  $\phi$
- construct the synchronised product  $\mathcal{A} \otimes \mathcal{B}_{\neg \phi}$
- $\bullet$  if its recognised language is empty, then  $\mathcal{A} \models \phi$

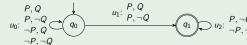






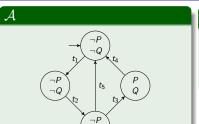


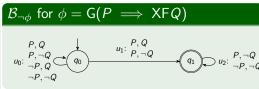
## $\mathcal{B}_{\neg \phi}$ for $\phi = \mathsf{G}(P \implies \mathsf{XF}Q)$

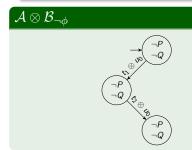


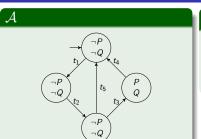
# $\mathcal{A}\otimes\mathcal{B}_{ eg\phi}$

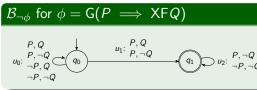


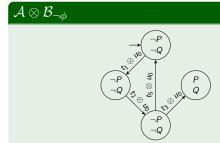


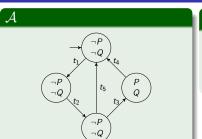


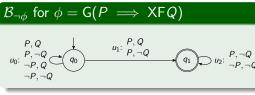


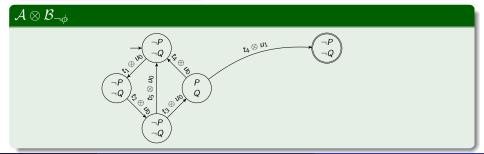


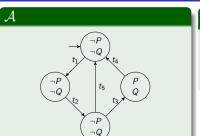


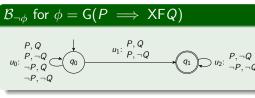


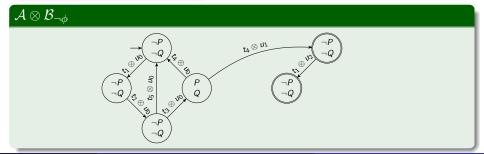


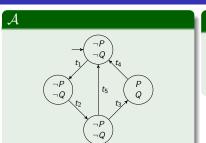


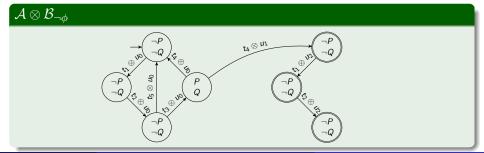


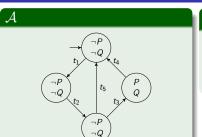


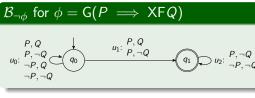


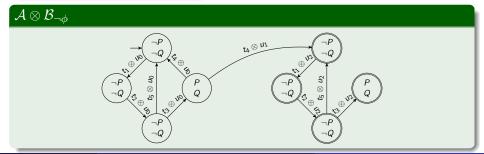


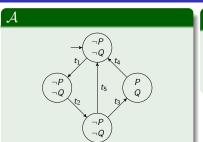


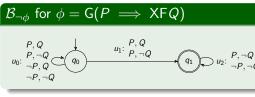


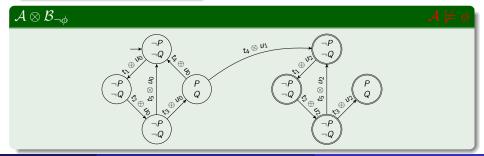


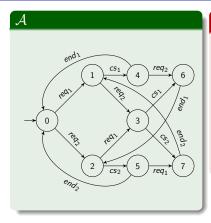




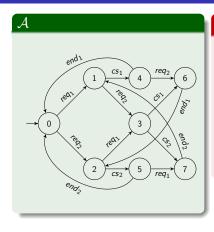




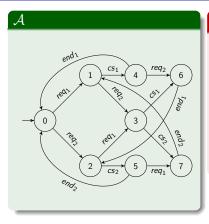


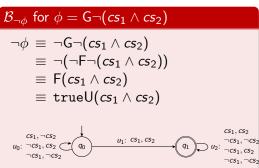


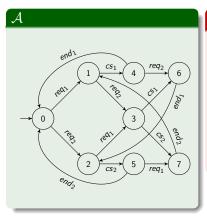
$$\mathcal{B}_{\neg\phi}$$
 for  $\phi = \mathsf{G}\neg(\mathit{cs}_1 \wedge \mathit{cs}_2)$ 



$$\mathcal{B}_{\neg\phi}$$
 for  $\phi = \mathsf{G}\neg(cs_1 \land cs_2)$ 
 $\neg \phi \equiv \neg \mathsf{G}\neg(cs_1 \land cs_2)$ 
 $\equiv \neg(\neg \mathsf{F}\neg(cs_1 \land cs_2))$ 
 $\equiv \mathsf{F}(cs_1 \land cs_2)$ 
 $\equiv \mathsf{true}\mathsf{U}(cs_1 \land cs_2)$ 







$$\mathcal{B}_{\neg\phi} \text{ for } \phi = \mathsf{G}\neg(cs_1 \land cs_2)$$

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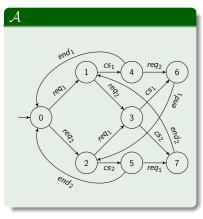
$$\equiv \neg(\neg \mathsf{F}\neg(cs_1 \land cs_2))$$

$$\equiv \mathsf{F}(cs_1 \land cs_2)$$

$$\equiv \mathsf{trueU}(cs_1 \land cs_2)$$

$$u_0: \neg cs_1, \neg cs_2 \qquad u_1: cs_1, cs_2 \qquad u_2: \neg cs_1, \neg cs_2 \qquad u_3: \neg cs_1, \neg cs_2 \qquad u_4: \neg cs_1, cs_2 \qquad u_5: \neg cs_1, \neg cs_2 \qquad u_5: \neg cs$$

 $\mathcal{A}\otimes\mathcal{B}_{\neg\phi}$ 



$$\mathcal{B}_{\neg\phi} \text{ for } \phi = \mathsf{G}\neg(cs_1 \land cs_2)$$

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$$\equiv \mathsf{F}(cs_1 \land cs_2)$$

$$\equiv \mathsf{true} \mathsf{U}(cs_1 \land cs_2)$$

$$u_1: cs_1, cs_2 \downarrow u_2: \neg cs_1, cs_2 \neg cs_1, c$$

#### $\mathcal{A}\otimes\mathcal{B}_{\neg\phi}$

 $\mathcal{A} \models \phi$ 

All transitions of A synchronise with  $u_0$ . So there is no accepting state and the formula is true.

#### Outline

- Symbolic model-checking
  - Computation of state sets
  - Binary Decision Diagrams
  - Automata representation

# Motivation for symbolic approaches

- state space explosion problem
  - main obstacle with model-checking algorithms
  - because of the necessity to construct the state space
- represent symbolically states and transitions
- it aims at representing concisely large sets of states

# Symbolic computation of state sets

Let  $A = \langle Q, E, T, q_0, I, F \rangle$  be an automaton, and  $S \subseteq Q$  a set of its states. Let  $\phi$  be a CTL formula.

#### **Notations**

- $Pre(S) = \{q \in Q \mid (q, \_, q') \in T \land q' \in S\}$  is the set of immediate predecessors of states in S
- $Sat(\phi) = \{q \in Q \mid q \models \phi\}$  is the set of states of the automaton satisfying formula  $\phi$
- Pre\*(S) is the set of predecessors of states in S, whatever the number of steps

# Computing $Sat(\phi)$

```
Sat(\neg \phi) = Q \setminus Sat(\phi)

Sat(\psi_1 \land \psi_2) = Sat(\psi_1) \cap Sat(\psi_2)

Sat(\mathsf{EX}\phi) = Pre(Sat(\phi))

Sat(\mathsf{AX}\phi) = Q \setminus Pre(Q \setminus Sat(\phi))

Sat(\mathsf{EF}\phi) = Pre^*(Sat(\phi))
```

# Symbolic features

- symbolic representations of the state sets
- functions to manipulate these symbolic representations

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- symbolic representations of the state sets
- functions to manipulate these symbolic representations

- suppose the automaton has 2 integer variables  $a,b \in \{0,\ldots,255\}$
- ullet each state is a triple  $(q, v_a, v_b)$  where  $v_a$  and  $v_b$  are values for a and b
- ullet the set of reachable states can contain  $|Q| \times 256 \times 256$  states (huge!)
- a possible symbolic representation could be  $(q_2,3,\_)$  for all states in  $q_2$  with a=3 and any value for b

# Requirements for symbolic model-checking

- **9** symbolic representation of Sat(p) for each proposition  $p \in Prop$
- 2 algorithm to compute a symbolic representation of Pre(S) from a symbolic representation of S
- algorithms to compute the complement, union and intersection of symbolic representations of the sets
- algorithm to compare symbolic representations of sets

# Binary Decision Diagrams

- data structure commonly used for the symbolic representation of state sets
- Efficiency: cheap basic operations, compact data structure
- Simplicity: data structure and associated algorithms simple to describe and implement
- Easy adaptation: appropriate for problems dealing with loosely correlated data
- Generality: not tied to a particular family of automata

#### BDD structure

#### *n* boolean variables $x_1, \ldots, x_n$

• suppose n = 4.  $\langle b_1, b_2, b_3, b_4 \rangle$  associates values with  $x_1, \ldots, x_4$ 

#### BDD structure

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- suppose n=4.  $\langle b_1,b_2,b_3,b_4\rangle$  associates values with  $x_1,\ldots,x_4$
- Let us represent  $S = \{ \langle b_1, b_2, b_3, b_4 \rangle \mid (b_1 \vee b_3) \wedge (b_2 \implies b_4) \}$

#### BDD structure

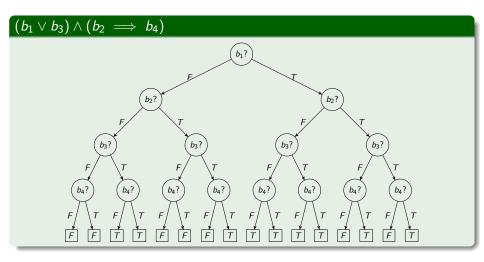
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- Possible representations:

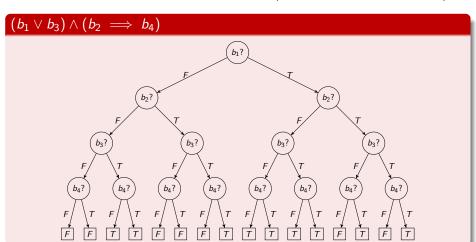
$$S = \{ \langle F, F, T, F \rangle, \langle F, F, T, T \rangle, \langle F, T, T, T \rangle, \langle F, F, T, T, T \rangle, \langle T, F, F, F, F \rangle, \langle T, F, F, F, F, T \rangle, \langle T, F, T, T, T \rangle, \langle T, F, T, T, T, T, T \rangle \}$$

- the formula itself:  $(b_1 \lor b_3) \land (b_2 \implies b_4)$
- the formula in disjunctive normal form:  $(b_1 \land \neg b_2) \lor (b_1 \land b_4) \lor (b_3 \land \neg b_2) \lor (b_3 \land b_4)$
- a decision tree

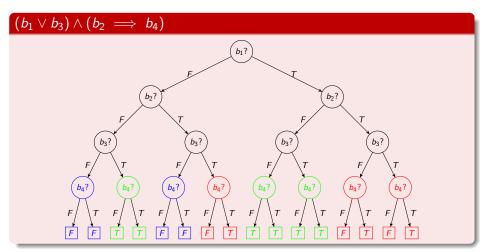
# Representation with a decision tree



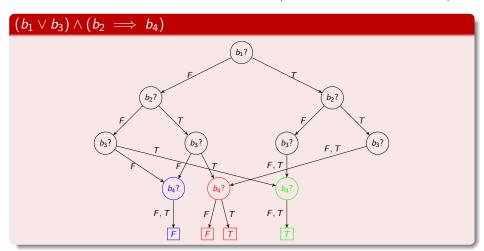
- identical subtrees are shared → directed acyclic graph (dag)
- internal superfluous nodes are deleted (where no choice is possible)



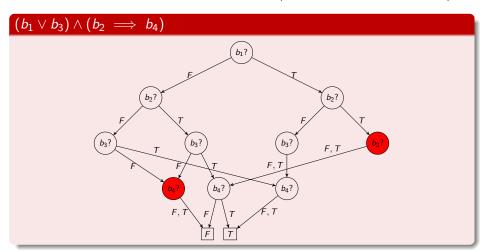
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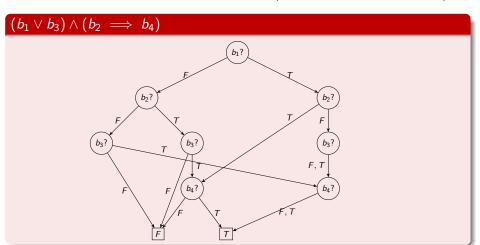
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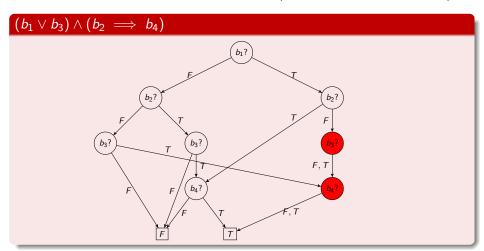
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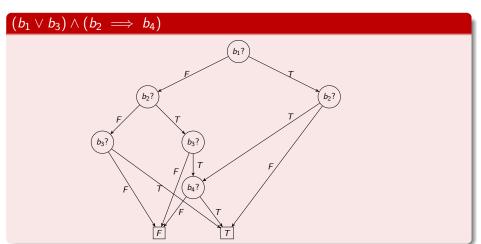
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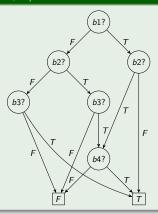


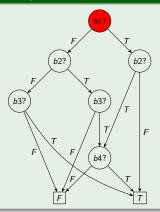
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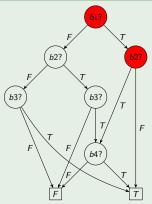


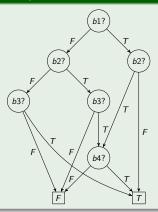
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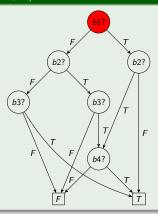












# Are $\langle T, F, T, F \rangle$ , $\langle F, F, T, F \rangle$ in S?

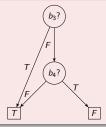
*b*4?

BDD for 
$$\neg((b_1 \land (b_2 \lor b_4) \land b_5) \lor \neg b_3) \lor (b_4 \implies (b_3 \land b_5))$$

BDD for 
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BDD for  $\neg((b_1 \land (b_2 \lor b_4) \land b_5) \lor \neg b_3) \lor (b_4 \implies (b_3 \land b_5))$  with ordering  $b_3$ ,  $b_4$ ,  $b_5$ ,  $b_1$ ,  $b_2$ 

BDD for  $\neg((b_1 \land (b_2 \lor b_4) \land b_5) \lor \neg b_3) \lor (b_4 \implies (b_3 \land b_5))$  with ordering  $b_3$ ,  $b_4$ ,  $b_5$ ,  $b_1$ ,  $b_2$ 



# Advantages of BDDs

- small representations
- existence of a canonical BDD structure :
  - unicity for a fixed order of the variables
  - test the equivalence of two symbolic representations

• test the emptyness

• simple operations: complement, union, intersection, projection

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#### Identical canonical BDDs

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# Advantages of BDDs

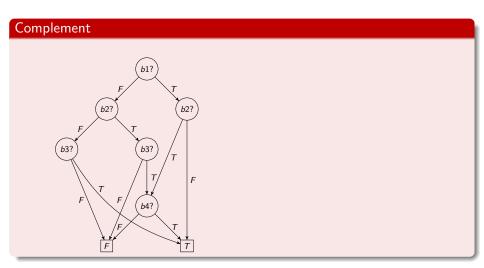
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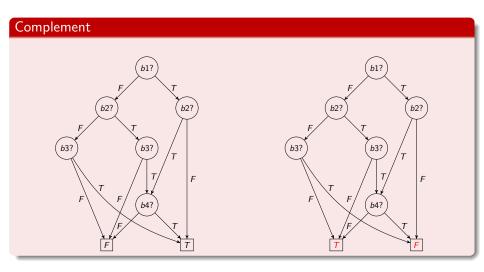
#### Identical canonical BDDs

test the emptyness

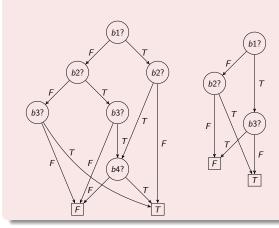
#### Reduced to the F leaf

• simple operations: complement, union, intersection, projection

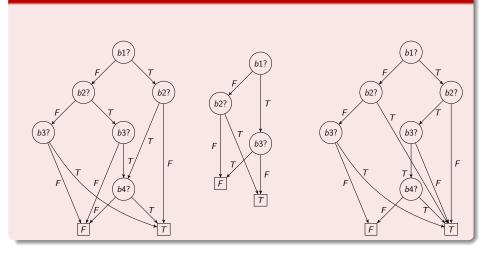




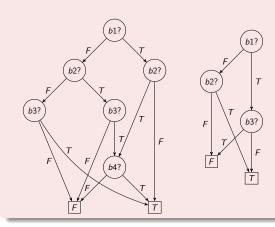
### Union



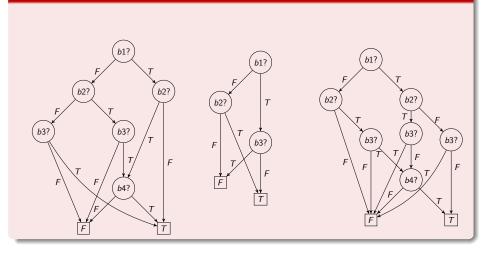
### Union



#### Intersection



#### Intersection



# Projection $S[b_3 := T]$ *b*1? b2? b2? *b*3? Ь3? F *b*4?

# Projection $S[b_3 := T]$ *b*1? b2? b2? Ь3? *b*3? *b*4? *b*4?

# Representing automata by BDDs

#### **Encoding of states**

- boolean encoding of states
- boolean encoding of each individual variable

Let us consider an automaton with:

- $Q = \{q_0, \ldots, q_6\}$
- an integer variable  $digit \in \{0, \dots, 9\}$
- a boolean variable *ready*

It can be encoded with 8 bits. For example,  $\langle q_3, 8, F \rangle$  is represented by:

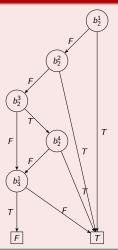
$$(\overbrace{F,T,T}^{q_3},\overbrace{T,F,F,F}^{8},\overbrace{F},F)_{b_1^1\ b_1^2\ b_1^2\ b_2^2\ b_2^3\ b_2^4\ b_3^1}^{8})$$

### Representing a set of states

$$Sat(ready \implies (digit > 2))$$

### Representing a set of states

### $Sat(ready \implies (digit > 2))$



### Representing a transition

#### Transition seen as a pair of states

### Outline

- 6 Reachability Properties
  - Reachability in temporal logic
  - Computation of the reachability graph

#### How to characterise reachability properties?

A reachability property states that some particular situation can be reached.

It may:

- be simple
- be conditional: restrict the form of paths reaching the state
- apply to any reachable state

Often, the negation of reachability is the interesting property.

- we can obtain n < 0
- we can enter the critical section
- we cannot have n < 0
- we cannot reach the crash state
- we can enter the critical section without traversing n = 0
- we can always return to the initial state
- we can return to the initial state

- we can obtain n < 0 (simple)
- we can enter the critical section
- we cannot have n < 0
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- we can obtain n < 0 (simple)
- we can enter the critical section (simple)
- we cannot have n < 0 (negation)
- we cannot reach the crash state
- we can enter the critical section without traversing n = 0
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- we can obtain n < 0 (simple)
- we can enter the critical section (simple)
- we cannot have n < 0 (negation)
- we cannot reach the crash state (negation)
- we can enter the critical section without traversing n = 0
- we can always return to the initial state
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- we can obtain n < 0 (simple)
- we can enter the critical section (simple)
- we cannot have n < 0 (negation)
- we cannot reach the crash state (negation)
- we can enter the critical section without traversing n = 0 (conditional)
- we can always return to the initial state
- we can return to the initial state

- we can obtain n < 0 (simple)
- we can enter the critical section (simple)
- we cannot have n < 0 (negation)
- we cannot reach the crash state (negation)
- we can enter the critical section without traversing n = 0 (conditional)
- we can always return to the initial state (any reachable state)
- we can return to the initial state

- we can obtain n < 0 (simple)
- we can enter the critical section (simple)
- we cannot have n < 0 (negation)
- we cannot reach the crash state (negation)
- we can enter the critical section without traversing n = 0 (conditional)
- we can always return to the initial state (any reachable state)
- we can return to the initial state (simple)

#### Form of formulae in CTL

- use the EF combinator:  $EF\phi$
- $\bullet$   $\phi$  is a propositional formula without temporal combinators
- E\_U\_ for conditional reachability
- nesting AG and EF when considering any reachable state

- we can obtain n < 0:
- we can enter the critical section:
- we cannot have n < 0:
- we cannot reach the crash state:
- we can enter the critical section without traversing n = 0:
- we can always return to the initial state:
- we can return to the initial state:

- we can obtain n < 0: EF(n < 0)
- we can enter the critical section:
- we cannot have n < 0:
- we cannot reach the *crash* state:
- we can enter the critical section without traversing n = 0:
- we can always return to the initial state:
- we can return to the initial state:

- we can obtain n < 0: EF(n < 0)
- we can enter the critical section: EFcs
- we cannot have n < 0:
- we cannot reach the crash state:
- we can enter the critical section without traversing n = 0:
- we can always return to the initial state:
- we can return to the initial state:

- we can obtain n < 0: EF(n < 0)
- we can enter the critical section: EFcs
- we cannot have n < 0:  $\neg \mathsf{EF}(n < 0) \equiv \mathsf{AG}(n \ge 0)$
- we cannot reach the crash state:
- we can enter the critical section without traversing n = 0:
- we can always return to the initial state:
- we can return to the initial state:

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- we cannot have n < 0:  $\neg \mathsf{EF}(n < 0) \equiv \mathsf{AG}(n \ge 0)$
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- we can enter the critical section without traversing n = 0:
- we can always return to the initial state:
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- we can obtain n < 0: EF(n < 0)
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- we can enter the critical section without traversing n = 0:  $E(n \neq 0)Ucs$
- we can always return to the initial state:
- we can return to the initial state:

- we can obtain n < 0: EF(n < 0)
- we can enter the critical section: EFcs
- we cannot have n < 0:  $\neg \mathsf{EF}(n < 0) \equiv \mathsf{AG}(n \ge 0)$
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- we can enter the critical section without traversing n = 0:  $E(n \neq 0)Ucs$
- we can always return to the initial state: AGEFinit
- we can return to the initial state:

- we can obtain n < 0: EF(n < 0)
- we can enter the critical section: EFcs
- we cannot have n < 0:  $\neg \mathsf{EF}(n < 0) \equiv \mathsf{AG}(n \ge 0)$
- we cannot reach the *crash* state:  $\neg \mathsf{EF} \mathit{crash} \equiv \mathsf{AG} \neg \mathit{crash}$
- we can enter the critical section without traversing n = 0:  $E(n \neq 0)Ucs$
- we can always return to the initial state: AGEFinit
- we can return to the initial state: EFinit

# Computation of the reachability graph

### Forward chaining

- start from the initial state
- add its successors
- continue until saturation

Drawback: potential explosion of the set being constructed

# Computation of the reachability graph

#### Backward chaining

Construct the set of states which can lead to some target states

- start from target states
- add their immediate predecessors
- continue until saturation
- test whether some initial state is in the computed set

#### Drawbacks:

- identify target states
- computing predecessors can be more difficult than computing successors (e.g. for automata with variables)
- target states may be unreachable

# Computation of the reachability graph

### On-the-fly exploration

- check the property during exploration
- only partially construct the state space