

Sokendai Lectures Tokyo, Japan 物理情報システムのための形式手法



Timed model checking – Part 1 Finite-state automata

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Partie 1: Untimed model checking – Plan

- 1 Model-checking in a nutshell
- 2 Finite-state automata
- 3 Temporal logics
- 4 Reachability
- 5 Specifying properties using observers

Context: Verifying complex timed systems

- Need for early bug detection
 - Bugs discovered when final testing: expensive
 - ightarrow Need for a thorough specification and verification phase









The Therac-25 radiation therapy machine (1/2)

- Radiation therapy machine used in the 1980s
- Involved in accidents between 1985 and 1987, in which patients were given massive overdoses of radiation
 - Approximately 100 times the intended dose!
 - Numerous causes, including race condition

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 - Approximately 100 times the intended dose!
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"The failure only occurred when a particular nonstandard sequence of keystrokes was entered on the VT-100 terminal which controlled the PDP-11 computer: an X to (erroneously) select 25MV photon mode followed by ↑, E to (correctly) select 25 MeV Electron mode, then Enter, all within eight seconds."

The Therac-25 radiation therapy machine (2/2)

The testing engineers could obviously not detect this strange (and quick!) sequence leading to the failure.

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Limits of testing

This case illustrates the difficulty of bug detection without formal methods.

Bugs can be difficult to find

...and can have dramatic consequences for critical systems:

- health-related devices
- aeronautics and aerospace transportation
- smart homes and smart cities
- military devices
- etc.

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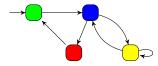
Hence, high need for formal verification

Outline

1 Model-checking in a nutshell

- 2 Finite-state automata
- 3 Temporal logics
- 4 Reachability
- 5 Specifying properties using observers

Use formal methods [Baier and Katoen, 2008]





A property to be satisfied

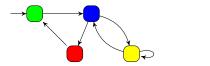
A model of the system

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Use formal methods [Baier and Katoen, 2008]





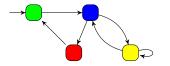
A property to be satisfied

A model of the system

Question: does the model of the system satisfy the property?

?

Use formal methods [Baier and Katoen, 2008]



is unreachable

A model of the system

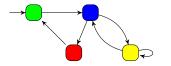
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Transition systems

Definition (Transition system)

A transition system (TS) is a tuple $\mathcal{TS} = (S, \Sigma, S_0, S_F, \Rightarrow)$, where

- S is a set of states;
- \blacksquare Σ is an alphabet of events;
- $S_0 \subseteq S$ is a set of initial states;
- $S_F \subseteq S$ is a set of final (or accepting) states; and,
- $\blacksquare \Rightarrow : S \times \Sigma \to 2^S$ is a transition relation.

Usually, we write $s_1 \stackrel{a}{\Longrightarrow} s_2$ when $(s_1, a, s_2) \in \Rightarrow$.

Outline

Model-checking in a nutshell

2 Finite-state automata

Syntax

- Semantics
- Examples
- Composing finite state automata
- 3 Temporal logics
- 4 Reachability
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Finite-state automata

Definition (Finite automaton)

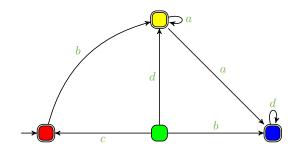
A Finite automaton (FA) $FA = (L, \Sigma, \ell_0, L_F, E)$ is a tuple where

- L is a finite set of locations;
- **\Sigma** is a finite set of actions;
- $\ell_0 \in L$ is the initial location;
- $L_F \subseteq L$ is a set of final (or accepting) locations;
- $E: L \times \Sigma \rightarrow L$ is a transition relation.

Usually, we write $l_1 \xrightarrow{a} l_2$ when $(l_1, a, l_2) \in E$.

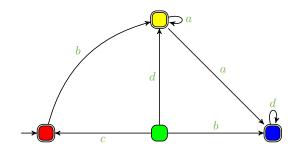
 $FA = (L, \Sigma, \ell_0, L_F, E), \text{ with}$ $L = \{l_1, l_2, l_3\}$ $\Sigma = \{a, b, c, d\}$ $\ell_0 = l_1$ $L_F = \{l_2\}$ $E = \{(l_1, a, l_1), (l_1, b, l_2), (l_2, c, l_1), (l_2, d, l_2), (l_3, b, l_2)\}$

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Semantics

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Semantics of finite automata

Definition (Semantics of finite automata)

Let $FA = (L, \Sigma, \ell_0, L_F, \Rightarrow)$ be a Finite Automaton. The semantics of FA is the transition system $\mathcal{TS} = (S, \Sigma, S_0, S_F, \Rightarrow)$, with

- $\bullet S = L;$
- \blacksquare Σ the same;
- $S_0 = \{\ell_0\};$
- \blacksquare $S_F = L_F$; and,
- $\Rightarrow = E.$

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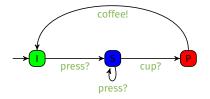
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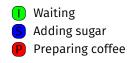
Model-checking in a nutshell

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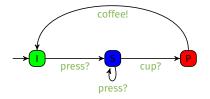
A coffee machine \mathcal{A}_C

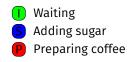




- Examples of runs
 - Coffee with no sugar

A coffee machine \mathcal{A}_C



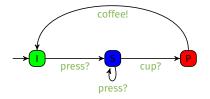


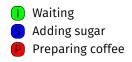
Examples of runs

Coffee with no sugar

Coffee with 2 doses of sugar

A coffee machine \mathcal{A}_C





Examples of runs

Coffee with no sugar

Coffee with 2 doses of sugar

And so on

A coffee drinker (1/2)

- Specify a coffee drinker automaton A_{D1} that performs forever the following actions:
 - press the button once
 - 2 place the cup
 - 3 wait for the coffee
 - 4 drink the coffee
 - 5 put the cup to the washing machine

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Specify a coffee drinker automaton A_{D2} that works just as A_{D1} except that (s)he can nondeterministically ask for 0, 1 or 2 doses of sugar.

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A washing machine

Specify a washing machine automaton A_W that accepts cups to wash, and once 5 cups are placed into the washing machine, then the machine washes all cups.

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Systems as components

Often, a complex system is made of components or modules Components can interact with each other:

- using strong synchronization
- using shared variables
- using one-to-one synchronization
- in an interleaving manner

Here, we show that FAs can be composed easily using strong synchronization on actions.

 $FA_1 = (L_1, \Sigma_1, (\ell_0)_1, (L_F)_1, E_1)$ $FA_2 = (L_2, \Sigma_2, (\ell_0)_2, (L_F)_2, E_2)$

Then we define $FA_1 \parallel FA_2$ as

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Draw the automaton composed of the automata $\mathcal{A}_C \parallel \mathcal{A}_{D1}$

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Draw the automaton composed of the automata $\mathcal{A}_C \parallel \mathcal{A}_{D2}$

Draw the automaton composed of the automata $\mathcal{A}_C \parallel \mathcal{A}_{D2}$

Start to draw the automaton composed of the automata $A_C \parallel A_{D2} \parallel A_W$. What do you notice?

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Outline

Model-checking in a nutshell

2 Finite-state automata

3 Temporal logics

- Specifying properties using logics
- LTL
- CTL

4 Reachability

5 Specifying properties using observers

Temporal logics

Modal logics expressing timing information over a set of atomic propositions, and can be used to formally verify a model.

Some temporal logics:

- LTL (Linear Temporal Logic)
- CTL (Computation Tree Logic)
- MITL
- CTL*
- µ-calculus

[Pnueli, 1977]

[Clarke and Emerson, 1982]

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Some temporal logics:

- LTL (Linear Temporal Logic)
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- CTL*
- \blacksquare μ -calculus

Warning

Temporal logics express the ordering between events over time, but do not (in general) contain timed information.

Timed model checking - 1

[Pnueli, 1977]

[Clarke and Emerson, 1982]

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- Specifying properties using logics
- LTL
- CTL

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LTL (Linear Temporal Logic) [Pnueli, 1977]

LTL expresses formulas about the future of one path, using a set of atomic propositions ${\cal AP}$

Minimal syntax:

$$\varphi ::= p \in AP \mid \neg \varphi \mid \varphi \lor \varphi \mid \mathbf{X} \varphi \mid \varphi \mathbf{U} \varphi$$

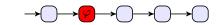
Explanation and additional operators:

- $p \in AP$ atomic proposition
- XNext"at the nexUUntil"ψ holds uFFinally (eventually)"now or soGGlobally"now and aRRelease
 - Weak until

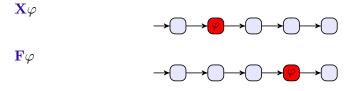
- "at the next step" " ψ holds until φ holds" "now or sometime later"
- "now and anytime later"

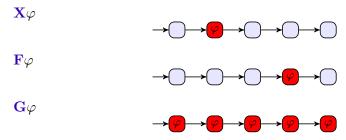
" ψ holds either until φ holds or forever"

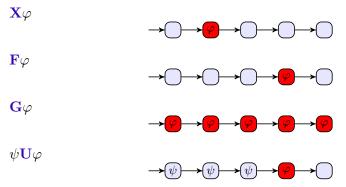
 $\mathbf{X}\varphi$

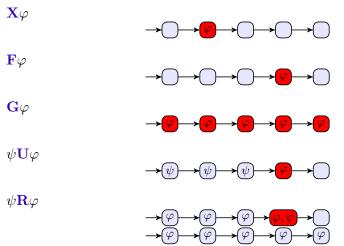


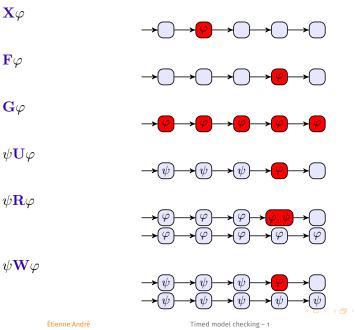
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On which states do the following properties hold?

<u>φ</u>
$\mathbf{X} \varphi$
$\mathbf{F}arphi$
$\overline{\mathbf{F}\psi}$
${f G}arphi$
$\mathbf{GX}(arphi \lor \psi)$
${f GF}arphi$
${f GF}\psi$
$\psi \mathbf{U} arphi$
$ \begin{array}{c} \varphi \mathbf{U} \psi \\ \psi \mathbf{W} \varphi \\ \hline \varphi \mathbf{W} \psi \\ \psi \mathbf{R} \varphi \end{array} $
$\psi \mathbf{W} arphi$
$arphi \mathbf{W} \psi$
$\overline{\psi \mathbf{R} arphi}$

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$\mathbf{F}\psi$	_
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${f GF}arphi$	
${f GF}\psi$	_
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Express in LTL the following properties:

"The plane will never crash" (safety property)

Express in LTL the following properties:

"The plane will never crash" (safety property)

- "The plane will never crash" (safety property)
- "I will eventually get a job" (liveness property)

- "The plane will never crash" (safety property)
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Express in LTL the following properties:

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• "Every day, I will be alive until the day of my death—unless I am immortal"

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- "If I ask for food infinitely often, then I will get food infinitely often" (strong fairness property)

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Outline

Model-checking in a nutshell

2 Finite-state automata

3 Temporal logics

- Specifying properties using logics
- LTL
- CTL

4 Reachability

5 Specifying properties using observers

CTL (Computation Tree Logic) [Clarke and Emerson, 1982]

CTL expresses formulas on the order between the future events for some or for all paths, using a set of atomic propositions AP

Quantifiers over paths:

$$\varphi ::= p \in AP \mid \neg \varphi \mid \varphi \lor \varphi \mid \mathbf{E}\psi \mid \mathbf{A}\psi$$

Quantifiers over states:

$$\psi ::= \mathbf{X}\varphi \mid \varphi \mathbf{U}\varphi$$

Explanation:

- E Exists "along some of the future paths"
- A ForAll "along all the future paths"

Illustrating combined quantifiers (1/2)

A path quantifier must always be followed by a state quantifier.

Some useful combinations:

Illustrating combined quantifiers (1/2)

A path quantifier must always be followed by a state quantifier.

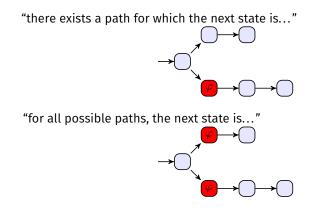
Some useful combinations:

"there exists a path for which the next state is..."

Illustrating combined quantifiers (1/2)

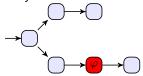
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Some useful combinations:



Illustrating combined quantifiers (2/2)

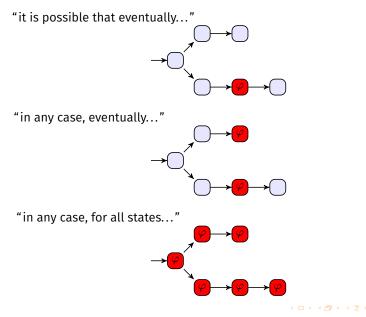
"it is possible that eventually..."

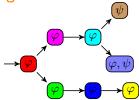


Illustrating combined quantifiers (2/2)

"it is possible that eventually..." "in any case, eventually..."

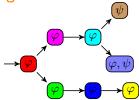
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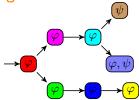
On which states do the following properties hold?

$\varphi \implies \psi$
$\mathbf{E}\mathbf{X}arphi$
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$\mathbf{AF}\psi$
$\mathbf{AX}(arphi \wedge \psi)$
${f EG}arphi$
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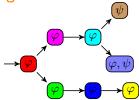
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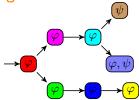
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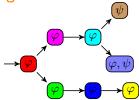
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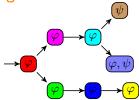
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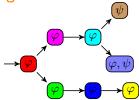
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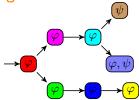
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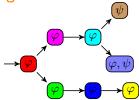
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Express in CTL the following properties:

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"I may eventually get a job"

"I may love you for the rest of my life"

(safety property)

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- "It can always happen that suddenly I discover formal methods and then I may use them for the rest of time"

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Express in CTL the following properties, and decide whether they are satisfied for the coffee machine

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Outline

- 1 Model-checking in a nutshell
- 2 Finite-state automata
- 3 Temporal logics
- 4 Reachability
- 5 Specifying properties using observers

The reachability problem

The reachability problem

Given FA, given a given location ℓ , does there exist a path from an initial location of FA leading to ℓ ?

Applications:

- Is there an execution of the therapy machine leading to the delivery of high radiations?
- Can the coffee machine deliver a coffee with five doses of sugar?

Forward reachability

Let ${\cal S}$ be the set of all reachable states.

Given a subset $S' \subseteq S$ of states, which states of S are reachable from S' in just one step?

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Let S be the set of all reachable states. Given a subset $S'\subseteq S$ of states, which states of S are reachable from S' in just one step?

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Given a set $S'\subseteq S$ of states, we define Post as:

$$Post(S') = \{s \in S \mid$$

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Given a set $S' \subseteq S$ of states, we define Post as:

$$Post(S') = \{s \in S \mid$$

By extension, we write $Post^*(S')$ for the set of all states reachable from states of S'.

Algorithm $isReachable(\mathcal{TS}, S_0, S_F)$

input : Set S_0 of initial states, set S_F of final states output : true if S_F is reachable from S_0 , false otherwise

1 $S \leftarrow S_0$;

Algorithm $isReachable(\mathcal{TS}, S_0, S_F)$

,

input : Set S_0 of initial states, set S_F of final states output: true if S_F is reachable from S_0 , false otherwise

1
$$S \leftarrow S_0$$
 ;

2 repeat

$$\mathbf{3} \quad | \quad \mathbf{if} \ S \cap S_F \neq \emptyset$$
 then

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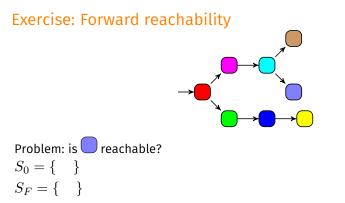
```
3 | if S \cap S_F \neq \emptyset then

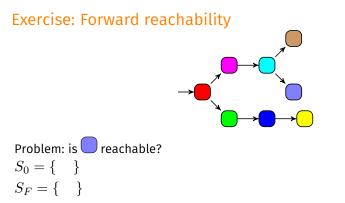
\downarrow;

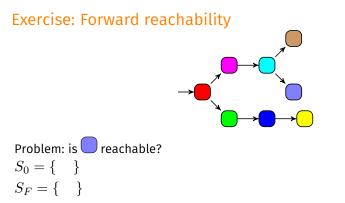
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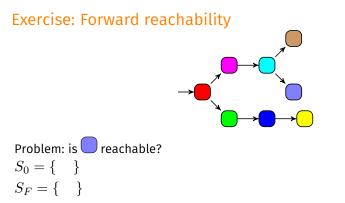
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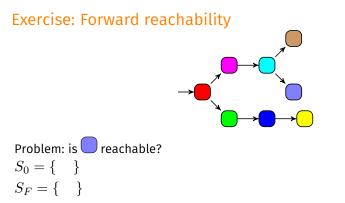
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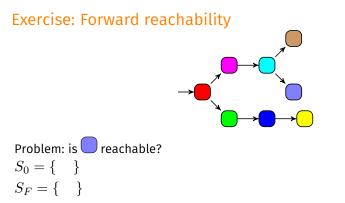


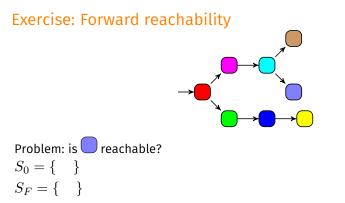


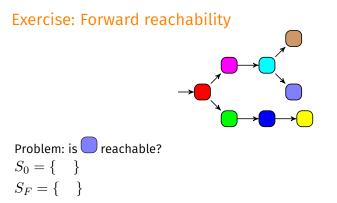












Answer:

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Backward Reachability

Let S be the set of all reachable states. Given a subset $S' \subseteq S$ of states, from which states of S can we access states of S' in just one step?

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Backward Reachability

Let S be the set of all reachable states. Given a subset $S' \subseteq S$ of states, from which states of S can we access states of S' in just one step?

Definition (Pre)

By extension, we write $Pre^*(S')$ for the set of all states from which one can reach states of S'.

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Timed model checking – 1

 $isReachableBack(\mathcal{TS}, S_0, S_F)$

input : Set S_0 of initial states, set S_F of final states output : true if S_F is reachable from S_0 , false otherwise

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 $isReachableBack(\mathcal{TS}, S_0, S_F)$

input : Set S_0 of initial states, set S_F of final states **output** : true if S_F is reachable from S_0 , false otherwise

- 1 $S \leftarrow S_F$;
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- 3

if $S \cap S_0 \neq \emptyset$ then

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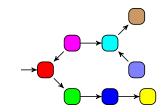
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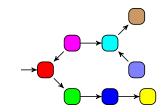
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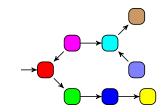
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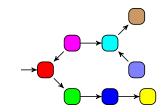




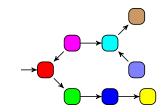




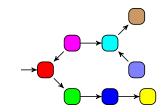




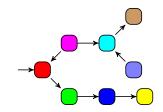














Answer:

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Timed model checking – 1

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Outline

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Verifying properties using observers

An observer is an automaton that observes the system behavior

- It synchronizes with other automata's actions
- It must be non-blocking (see example on the white board)
- Its location(s) give an indication on the system property

Then verifying the property reduces to a reachability condition on the observer (in parallel with the system)

Observers for the coffee machine (1/3)

Design an observer for the coffee machine and the drinker verifying that whenever the coffee comes, no cup was put to the washing machine before. (...and check the validity of the property)

Observers for the coffee machine (1/3)

Design an observer for the coffee machine and the drinker verifying that whenever the coffee comes, no cup was put to the washing machine before. (...and check the validity of the property)

Observers for the coffee machine (2/3)

Design an observer for the coffee machine and the drinker verifying that it is possible to order a coffee with at least one dose of sugar.

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Design an observer for the coffee machine and the drinker verifying that it is possible to order a coffee with at least one dose of sugar.

Observers for the coffee machine (3/3)

Design an observer for the coffee machine and the drinker verifying that it is possible to order a coffee with exactly one dose of sugar.

Observers for the coffee machine (3/3)

Design an observer for the coffee machine and the drinker verifying that it is possible to order a coffee with exactly one dose of sugar.

Sources and references

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General References

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Additional explanation

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Explanation for the 4 pictures in the beginning



Allusion to the Northeast blackout (USA, 2003) Computer bug Consequences: 11 fatalities, huge cost (Picture actually from the Sandy Hurricane, 2012)



Allusion to the sinking of the Sleipner A offshore platform (Norway, 1991) No fatalities Computer bug: inaccurate finite element analysis modeling (Picture actually from the Deepwater Horizon Offshore Drilling Platform)



Allusion to the MIM-104 Patriot Missile Failure (Iraq, 1991) 28 fatalities, hundreds of injured Computer bug: software error (clock drift) (Picture of an actual MIM-104 Patriot Missile, though not the one of 1991)



Error screen on the earliest versions of Macintosh

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Author: Étienne André

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