

# ON THE COMPLEXITY OF NORMALIZATION FOR THE PLANAR $\lambda$ -CALCULUS

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Recall that an untyped  $\lambda$ -term  $t$  is *linear* if there exists a list  $\Gamma$  – the list of free variables in  $t$  – such that  $\Gamma \vdash t$  is derivable with the rules below (with  $\Gamma$  and  $\Delta$  disjoint in *app*):

$$\frac{}{x \vdash x} \textit{var} \quad \frac{\Gamma \vdash t \quad \Delta \vdash u}{\Gamma, \Delta \vdash tu} \textit{app} \quad \frac{\Gamma, x \vdash t}{\Gamma \vdash \lambda x.t} \textit{lam} \quad \frac{\Gamma, y, x, \Delta \vdash t}{\Gamma, x, y, \Delta \vdash t} \textit{exc}$$

Call a linear  $\lambda$ -term  $t$  *planar* when there is an *ordered* list  $\Gamma$  such that  $\Gamma \vdash t$  is derivable in the subsystem *without the exc rule*: for example,  $\lambda x. \lambda y. f \ x \ y$  is planar but  $\lambda x. \lambda y. f \ y \ x$  is not. Planar  $\lambda$ -terms are closed under  $\beta$ -reduction. Furthermore, this notion is motivated by semantics (non-symmetric monoidal closed categories), topology (a linear  $\lambda$ -term is planar when its representation as a syntax tree with binding edges is a planar combinatorial map) and linguistics (in the Lambek calculus [Lam58], a precursor of linear logic).

Less attention has been paid, however, to the *computational* consequences of planarity. There is a recent implicit complexity result [NP20] using planar  $\lambda$ -terms, where general linear  $\lambda$ -terms would be too expressive. Here, we focus on the *complexity* of *normalizing*  $\lambda$ -terms, asking ourselves whether planarity lowers it. For linear (possibly non-planar)  $\lambda$ -terms, we know that:

**Theorem 0.1** ([Mai04]). *The following decision problem is P-complete under logarithmic space reductions:*

- *Input: two (untyped) linear  $\lambda$ -terms  $t$  and  $u$ .*
- *Output: are  $t$  and  $u$   $\beta$ -convertible, that is, do they have the same normal form?*

(Note that the complexity of the  $\beta$ -convertibility problem for simply typed (possibly non-linear)  $\lambda$ -terms is much higher, namely TOWER-complete – this is implicit in [Sta79], as explained in [Ngu23].)

**We believe that this problem is still P-complete when  $t$  and  $u$  are planar.** Two years ago, we claimed this as a theorem<sup>1</sup> but the proposed proof – which purported to provide a logspace reduction from the Circuit Value Problem (CVP), just like Mairson’s proof of Theorem 0.1 – contained a subtle yet serious flaw, described at the end of Section 2.

In this extended abstract, we outline another attempt to reduce CVP to planar normalization.

## 1. THE CIRCUIT VALUE PROBLEM

For our purposes, a *boolean circuit* with  $n$  gates can be seen as a list of  $n$  equations defining the values of the boolean variables  $x_1, \dots, x_n$ , such as the following example:

$$x_1 := 1; \ x_2 := 0; \ x_3 := 1; \ x_4 = x_1 \wedge x_2; \ x_5 = \neg x_1; \ x_6 = x_5 \wedge x_3; \ x_7 = x_4 \vee x_6$$

Here, equations 4 to 7 define  $x_7 = (x_1 \wedge x_2) \vee (\neg x_1 \wedge x_3) = \text{if } x_1 \text{ then } x_2 \text{ else } x_3$ , so the final result of the circuit is (if 1 then 0 else 1) = 0. In each equation, the right-hand side contains either a constant 0/1 or the application of an operator  $\neg, \wedge, \vee$ . Furthermore, we require that in the latter case, the arguments given to the operator have been defined *before* the current equation; in other words, the enumeration  $x_1, x_2, \dots$  is a *topological ordering* of the circuit.

**Theorem 1.1** ([GHR95, Theorem 6.2.1]). *The Topologically Ordered Circuit Value Problem (TopCVP), defined below, is P-complete.*

- *Input: a topologically ordered boolean circuit, as in the above example.*
- *Output: the final value computed by the circuit.*

<sup>1</sup>In a talk at the Structure Meets Power 2021 workshop: <http://noamz.org/talks/smp.2021.06.28.pdf>

## 2. PLANAR BOOLEANS DO NOT SUFFICE

To encode the Circuit Value Problem in the linear  $\lambda$ -calculus, Mairson [Mai04] uses a linear encoding of booleans. Unfortunately, his encoding represents 0 as a non-planar  $\lambda$ -term, namely  $\lambda x. \lambda y. \lambda f. f y x$ .

A planar linear encoding of booleans has been introduced in [Ngu21, Chapter 7] to give a strictly *linear* variant of the previously mentioned result of [NP20], whose original statement used planar *affine*  $\lambda$ -terms.

$$\mathbf{false} = \lambda k. \lambda f. k f (\lambda x. x) \quad \mathbf{true} = \lambda k. \lambda f. k (\lambda x. x) f$$

While our reduction targets untyped  $\lambda$ -terms, it can be useful to think of these terms as the only inhabitants in normal form of the type

$$\mathbf{Bool} = \forall \alpha \beta. ((\alpha \multimap \alpha) \multimap (\alpha \multimap \alpha) \multimap \beta) \multimap (\alpha \multimap \alpha) \multimap \beta$$

This can be seen as the image, by a continuation-passing-style transformation, of an encoding using linear  $\lambda$ -terms *with pairs* proposed by Mitsuoka [Mat15] in his alternative proof of Theorem 0.1:

$$\mathbf{false}' = \lambda f. (f, \lambda x. x) \quad \mathbf{true}' = \lambda f. (\lambda x. x, f) \quad \mathbf{Bool}' = \forall \alpha. (\alpha \multimap \alpha) \multimap (\alpha \multimap \alpha) \otimes (\alpha \multimap \alpha)$$

We can also define boolean connectives acting on the encodings of [Ngu21] (we have  $\mathbf{cstt} b =_{\beta} \mathbf{true}$  and  $\mathbf{cstf} b =_{\beta} \mathbf{false}$  for  $b \in \{\mathbf{true}, \mathbf{false}\}$ ), using the notations  $\mathbf{id} = \lambda x. x$  and  $f \circ g = \lambda x. f (g x)$ :

$$\begin{aligned} \mathbf{cstt} &= \lambda b. \lambda k. \lambda f. b (\lambda g. \lambda h. k \mathbf{id} (g \circ h)) f \\ \mathbf{cstf} &= \lambda b. \lambda k. \lambda f. b (\lambda g. \lambda h. k (g \circ h) \mathbf{id}) f \\ \mathbf{not} &= \lambda b. b (\lambda g. \lambda h. g (\mathbf{cstt} (h \mathbf{true}))) \mathbf{cstf} \\ \mathbf{and} &= \lambda b_1. \lambda b_2. \lambda k. b_1 (\lambda f_1. b_2 (\lambda f_2. \lambda f_3. k (\lambda x. f_1 (f_2 x) f_3))) \end{aligned}$$

(disjunction can be derived by De Morgan's laws). This is enough to translate boolean *formulas* into planar linear  $\lambda$ -terms.

However, to transpose Mairson's methodology for encoding boolean circuits to the planar linear setting, we would need a planar  $\lambda$ -term  $\mathbf{copy}$  such that (similarly to the W combinator in Curry's BCKW)

$$\forall t \in \{\mathbf{true}, \mathbf{false}\}, \mathbf{copy} f t =_{\beta} f t t$$

We have not been able to find such a term; we did manage to define a planar  $\lambda$ -term  $\mathbf{copy}'$  that satisfies  $\mathbf{copy}' t f =_{\beta} f t t$ , but this is significantly different in a planar setting. Hence the gap in our previous attempt at reducing CVP to  $\beta$ -convertibility of planar  $\lambda$ -terms.

## 3. A NEW ENCODING OF TOPCVP

Our new idea is to work with an encoding of *bit vectors*, on which we implement the following operations:

$$\mathbf{not}_{i,n}(\langle x_1, \dots, x_n \rangle) = \langle x_1, \dots, x_n, \neg x_i \rangle \quad \mathbf{and}_{i,j,n}(\langle x_1, \dots, x_n \rangle) = \langle x_1, \dots, x_n, x_i \wedge x_j \rangle$$

and the analogous  $\mathbf{or}_{i,j,n}, \mathbf{false}_n, \mathbf{true}_n: \{0, 1\}^n \rightarrow \{0, 1\}^{n+1}$  for  $1 \leq i, j \leq n$ . Let also  $\mathbf{last}_n(\langle x_1, \dots, x_n \rangle) = x_n$ .

The value of our example circuit from Section 1 can then be expressed, using these operations, as

$$\mathbf{last}_7 \circ \mathbf{or}_{4,6,6} \circ \mathbf{and}_{5,3,5} \circ \mathbf{not}_{1,4} \circ \mathbf{and}_{1,2,3} \circ \mathbf{true}_2 \circ \mathbf{false}_1 \circ \mathbf{true}_0(\langle \rangle)$$

**3.1. Representation of bit vectors.** Unsurprisingly, we use the Church encoding of  $k$ -tuples, together with the above-mentioned type  $\mathbf{Bool}$ , to represent vectors of  $k$  bits. For instance,  $\langle 0, 1, 0 \rangle$  is encoded as

$$\overline{\langle 0, 1, 0 \rangle} = \lambda k. k \mathbf{false} \mathbf{true} \mathbf{false}$$

which should be seen as an inhabitant of the type

$$\mathbf{Bool}_3 = \forall \gamma. (\mathbf{Bool} \multimap \mathbf{Bool} \multimap \mathbf{Bool} \multimap \gamma) \multimap \gamma$$

**3.2. Implementing vectorial operations.** First, we implement  $\text{fetch}_{i,n}(\langle x_1, \dots, x_n \rangle) = \langle x_1, \dots, x_n, x_i \rangle$ :

$$\text{fetch}_{i,n} = \lambda v. v (\lambda x_1. \dots \lambda x_n. x_1 c_1 f_1 (\dots (x_n c_n f_n \overline{\langle 0, \dots, 0 \rangle}) \dots))$$

$$\text{where } c_j = \begin{cases} \lambda g. \lambda h. (g \circ (\lambda k. \lambda b_1. \dots \lambda b_{n+1}. k b_1 \dots b_{i-1} (\text{cstt } b_i) b_{i+1} \dots b_n (\text{cstt } b_{n+1})) \circ h) & \text{if } i = j \\ \lambda g. \lambda h. (g \circ (\lambda k. \lambda b_1. \dots \lambda b_{n+1}. k b_1 \dots b_{j-1} (\text{cstt } b_j) b_{j+1} \dots b_{n+1}) \circ h) & \text{otherwise} \end{cases}$$

$$\text{and } f_j = \begin{cases} \lambda k. \lambda b_1. \dots \lambda b_{n+1}. k b_1 \dots b_{i-1} (\text{cstf } b_i) b_{i+1} \dots b_n (\text{cstf } b_{n+1}) & \text{if } i = j \\ \lambda k. \lambda b_1. \dots \lambda b_{n+1}. k b_1 \dots b_{j-1} (\text{cstf } b_j) b_{j+1} \dots b_{n+1} & \text{otherwise} \end{cases}$$

Note that by replacing every  $\text{cstt } b_{n+1}$  by  $\text{cstf } b_{n+1}$  and vice versa, we get an implementation of  $\text{not}_{i,n}!$

We then set  $\text{and}_{i,j,n} = \text{and}'_n \circ \text{fetch}_{i,n+1} \circ \text{fetch}_{j,n}$  where  $\text{and}'_n$  implements an in-place conjunction

$$\text{and}'_n(\langle x_1, \dots, x_{n+2} \rangle) = \langle x_1, \dots, x_n, x_{n+1} \wedge x_{n+2} \rangle$$

To define  $\text{and}'_n$ , we reuse the planar  $\lambda$ -term  $\text{and}$  that implements the conjunction on the booleans of Section 2:

$$\text{and}'_n = \lambda v. \lambda k. v (\lambda x_1. \dots \lambda x_{n+2}. k x_1 \dots x_n (\text{and } x_{n+1} x_{n+2}))$$

Finally, we take:

$$\text{last}_{i,n} = \lambda v. v (\lambda x_1. \dots \lambda x_n. x_1 (\lambda g. \lambda h. g \circ h) \text{id} (\dots (x_{n-1} (\lambda g. \lambda h. g \circ h) \text{id } x_n) \dots))$$

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